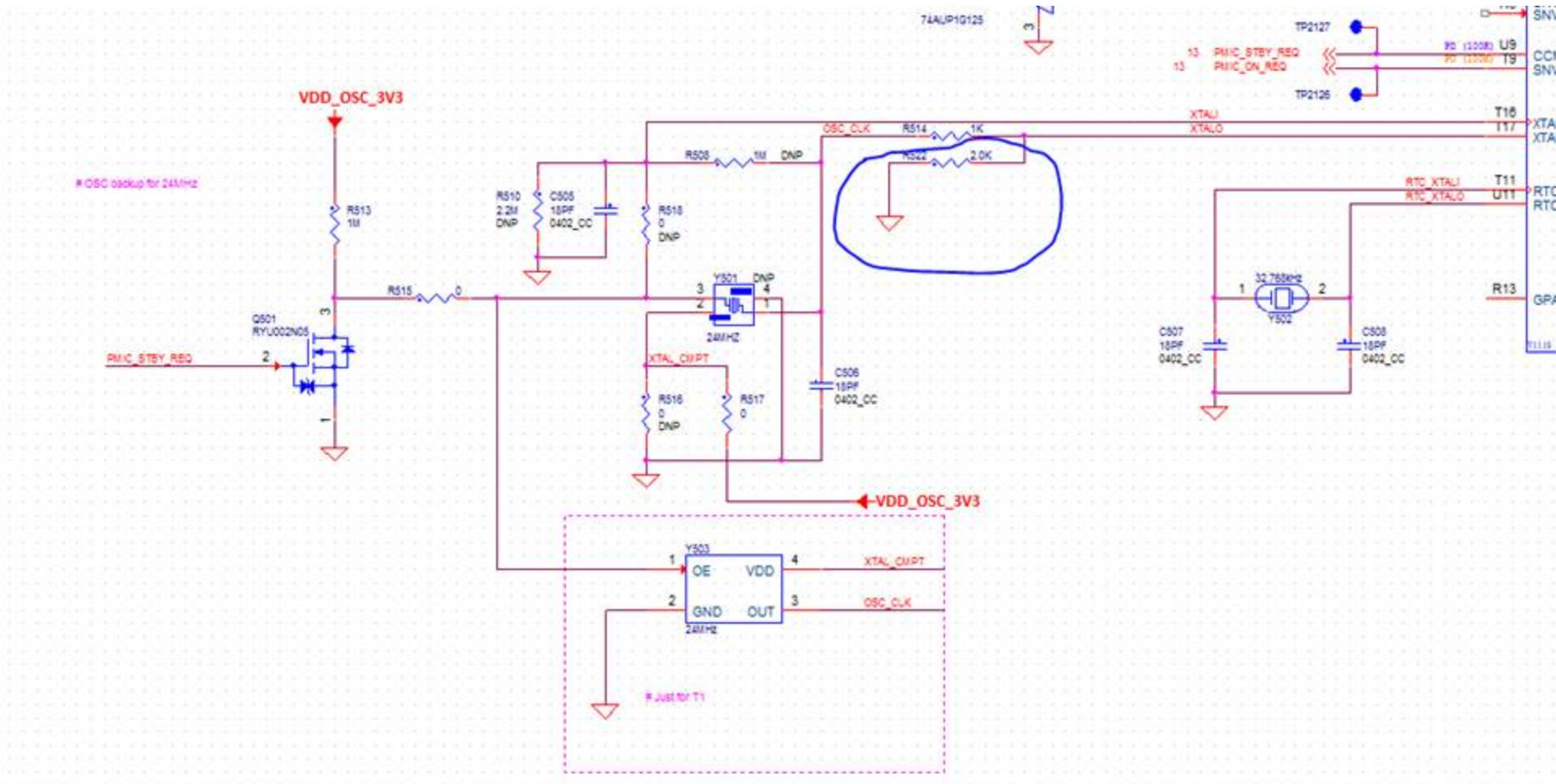
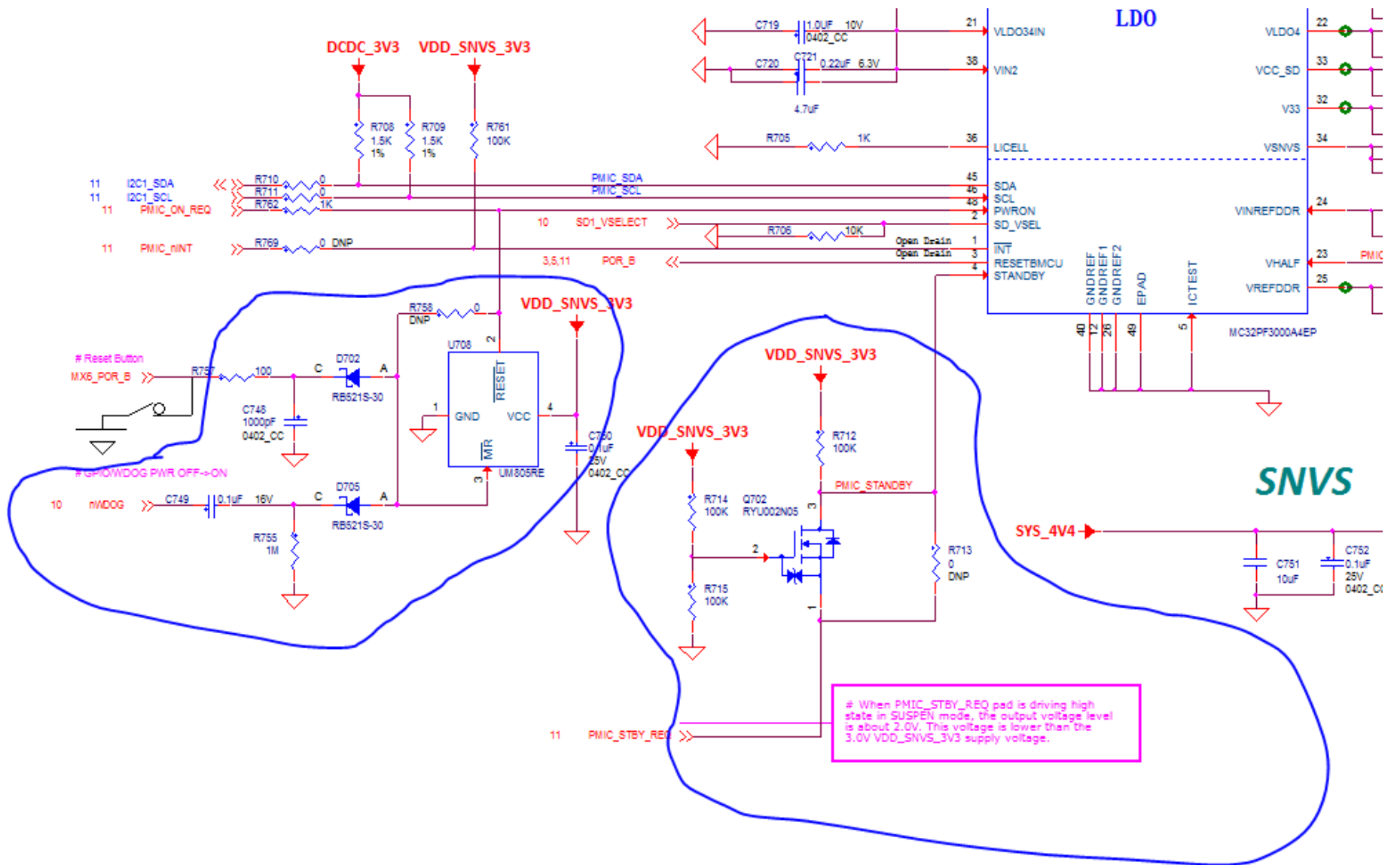


For the i.mx6UL (MCIMX6G3DVM05AA) design, there's a note on the reference schematic saying to use an external oscillator (rather than crystal) for "T1". Is that a preliminary version of the chip and do I need the oscillator or can I just use a crystal (both were in the reference design)? A couple of other interesting things/questions – why is there a resistor divider on XTALO and why is the oscillator driving XTALO and not XTALI?



There's also a strange circuit (U708+C749, R755, D705) on the PMIC PWRON pin. I need some help deciphering this and if it's needed, how software needs to configure nWDOG. Is Q702 still needed?



11 I2C1_SDA << R710 0
 11 I2C1_SCL << R711 0
 11 PMIC_ON_REQ << R762 1K
 11 PMIC_NINT << R769 0 DNP

Reset Button
 MX6_POR_B >> R757 100
 # GPDWDOG PWR OFF->ON
 nWDOG >> C749 0.1uF 16V

DCDC_3V3 VDD_SNV3_3V3

VDD_SNV3_3V3

VDD_SNV3_3V3

VDD_SNV3_3V3

11 PMIC_STBY_REQ >>

C719 1.0uF 10V 0402_CC
 C720 0.22uF 6.3V
 4.7uF
 R705 1K
 21 VLD034IN
 38 VIN2
 36 LICELL
 45 SDA
 46 SCL
 48 PWRON
 2 SD_VSEL
 1 INT
 3 RESETBMCU
 4 STANDBY

LDO

VLD04 22
 VCC_SD 33
 V33 32
 VSNVS 34
 VINREFDDR 24
 VHALF 23
 VREFDDR 25
 MC32PF3000A4EP

SYS_4V4

SNVS

C751 10uF
 C752 0.1uF 25V 0402_Ck

A bunch of questions about this circuit:

- Q2003 is permanently bypass – why is it in the circuit?
- Why is there an ESD diode on the output of Q2002?
- Why is there a power cut-off slide switch (SW2001) in addition to a ONOFF pushbutton switch on the PMIC?

SYS PWR

