

KEEPING YOU INFORMED – LCD AND LED AUTOMOTIVE DASHBOARD DISPLAYS

SECURE CONNECTED & AUTOMATED VEHICLES INSTRUMENT CLUSTERS & INFOTAINMENT

EMMANUEL NANA AMEC PRODUCT APPLICATION ENGINEER FTF-AUT-N1918 LONE STAR BALLROOM E - LEVEL 3 MONDAY 4:15PM MAY 2016

PUBLIC USE



AGENGA

- Overview
- LCD Display Drivers
- LED Controller
- Summary

Abstract – A modern car is more than just transport, it provides comfortable heating and cooling, navigation systems, entertainment systems, and a lot of data about the car's health and performance. Including how well you are conserving fuel or when to seek service. All of this is a challenge to present to the driver and passengers, without distraction to driving, under all conditions from bright daylight to the dark of night, in any temperature. We talk about both Liquid Crystal Display (LCD) and Light Emitting Diode (LED) component solutions that keep your dashboard simple, readable, and helpful.

PUBLIC USE FTF-AUT-N1918



OVERVIEW NXP'S INTERFACE **PRODUCTS FOR** AUTOMOTIVE



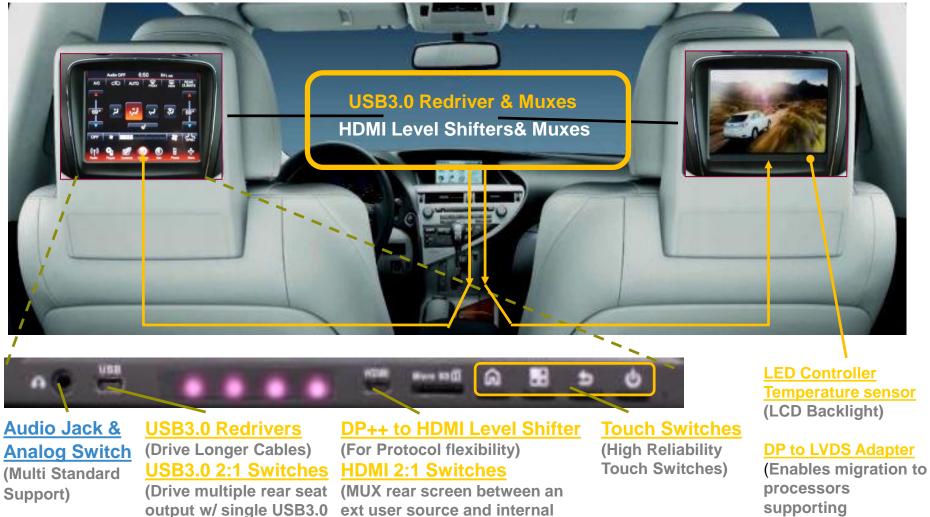
Interface Products – Front Seat Infotainment System peripherals





Interface Products – Rear Seat Infotainment

source on processor)



video player)

DisplayPort)



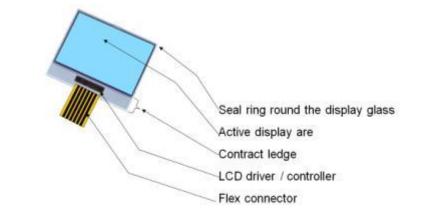
4 PUBLIC USE FTF-AUT-N1918

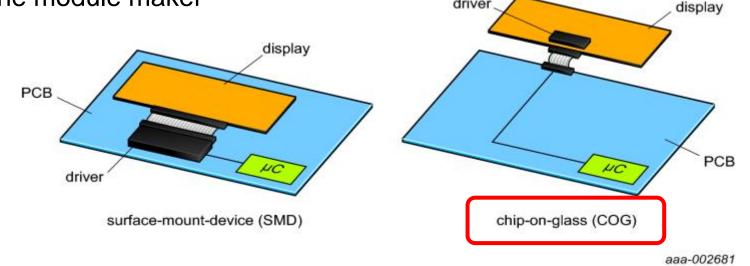
LCD DRIVERS



Chip-on-Glass (COG) vs Surface Mount Device (SMD)

- GOC cost-saving and easy way to design with LCD Drivers •
- Advantages
 - The driver is directly placed on the glass
 - No package is needed (cost saving)
 - Easier PCB design
 - Less board space needed on the PCB
 - Driver is handled by the module maker



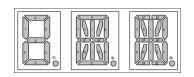


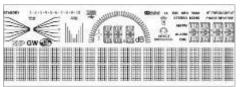
drive



NXP New LCD Driver Trends

- Higher Frame Frequency
 - Standard TN displays require frame frequency of 64Hz
 - High-contrast, true black background Vertical Alignment displays require frame frequency to be 2x to 3x higher
- On-Chip Charge Pump
 - Eliminate the need for external components
 - Ability to generate high VLCD voltage, even in systems with only 3.3V supply
 - In battery operated systems, the supply voltage may vary from 3.3V to 1.8V, but the charge pump allows the user to maintain a stable VLCD voltage
 - Ability to regulate VLCD internally and do temperature compensation
- Higher VLCD Voltage
 - For Vertical Alignment display applications (with true black background)
- Integrated Temperature Sensor
 - For temperature-compensated VLCD
 - With digital readout
- Temperature Compensated VLCD
 - Ability to adjust and maintain optimal contrast with varying liquid crystal viscosity due to temperature fluctuations
- Improved Power-on Reset (POR) Circuitry
 - Improved system ESD performance
 - Up to ±15kV air discharge and ±8kV contact discharge
- 8 PUBLIC USE FTF-AUT-N1918

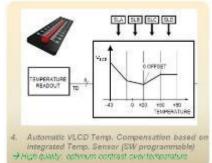






V_{1.3} Voltage independent of VDD Voltage V_{1.5} Voltage programmable (electronic tuning) Cast be used with a whore range of LLCS (Car cod) Dat be used in a whore range of applications



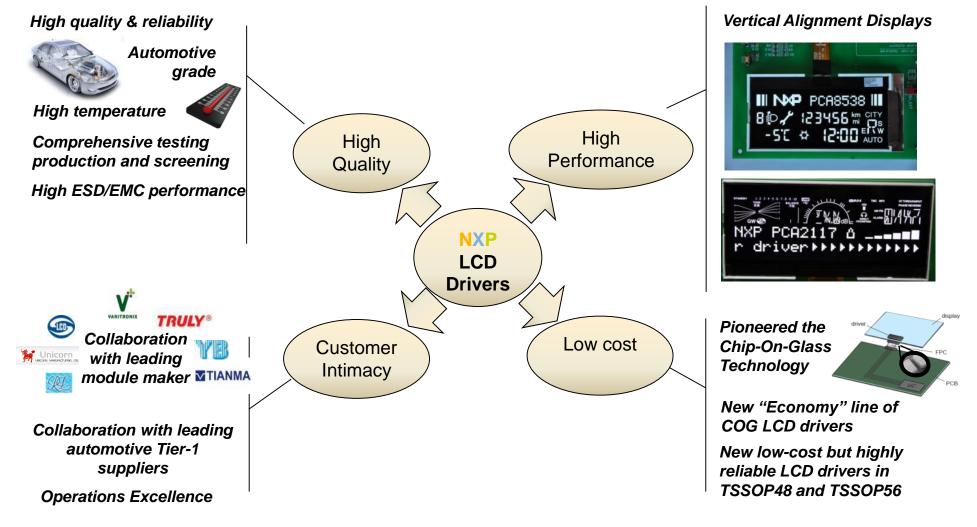








NXP LCD Drivers – Key Values at a Glance

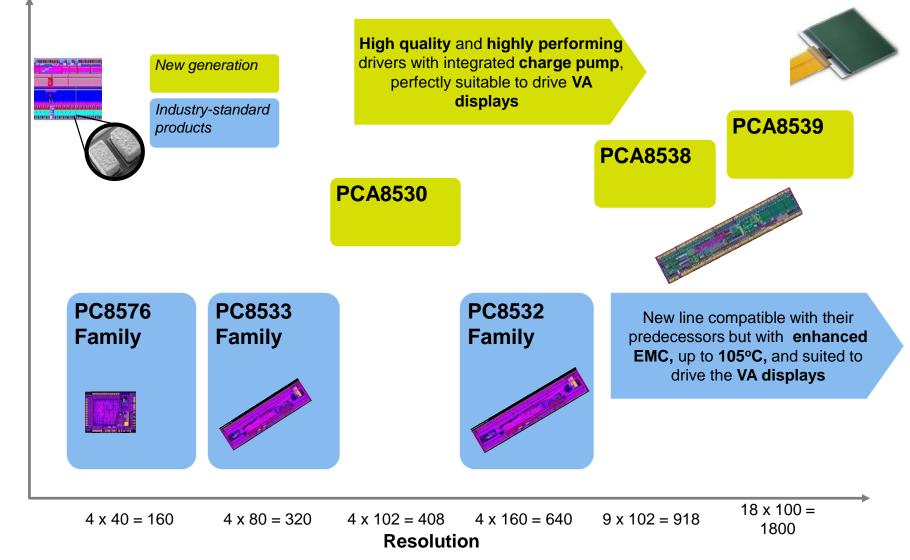




COG LCD DRIVERS



COG LCD Segment Drivers for Automotive Applications



Performance

11

PUBLIC USE

FTF-AUT-N1918



NEW! The most performing mux 1:4 display driver in the market!

Key Features

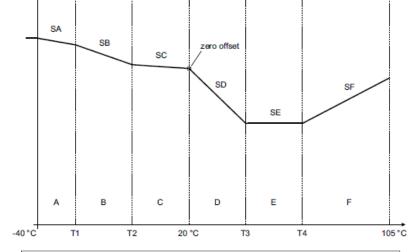
- 4 x 102 segment driver (408 dots or segments)
- Programmable Multiplex Rate (1:1, 1:2, 1:4)
- n-line inversion (includes line and frame inversion)
- On-chip **Charge pump** with integrated capacitors for the V_{LCD} internal generation up to $12 \ V$
- Temperature readout and device status readout for diagnostics
- Temperature compensated VLCD voltage (with programmable temperature regions and programmable slopes)
- Programmable frame frequency 45Hz to **300Hz**
- I²C-bus and SPI-bus Interface
- Up to 4 chips can be cascaded with internal or external V_{LCD} to drive bigger display
- Extended temperature range up to +105°C
- AEC-Q100 grade 2 compliant

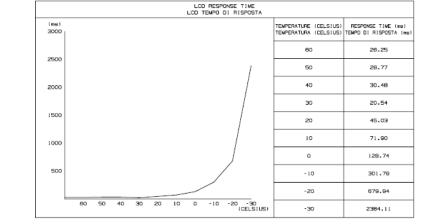
Mux 1:4 LCD driver with integrated charge pump and temperature sensor

- High and stable display contrast over temperature
- Compensation for slow switching times at cold temperatures
- Can drive big segments
- Can be used in combination with high-res ITO for cost optimization
- High resistance to EMI
- High precision of V_{LCD} and frame frequency
- Highly featured



- Accurate VLCD temperature compensation
 - High and stable display contrast across the temperature
 - Compensation for degradation of switching times at cold temperatures



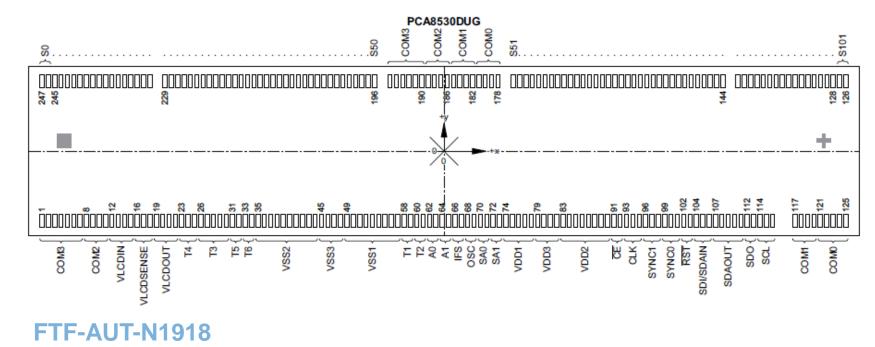


T1T[2:0] to	Temperature regio	on 1 and 2	Temperature region 3 and 4			
T4T[2:0]	T1, T2 (°C)	Corresponding TD value[1]	T3, T4 (°C)	Corresponding TD value <u>[1]</u>		
000	-34	10	+29	110		
001	-27	20	+38	124		
010	-21	30	+47	138		
011	-15	40	+55	152		
100	-9	50	+64	166		
101	-2	60	+73	180		
110	+4	70	+82	194		
111	+10	80	+91	208		

TSA[2:0] to TSF[2:0] value	Slope factor (mV/°C)	Temperature coefficients SA to SF ^[1]
000	0	0.000
001	-6	-0.125
010	-12	-0.250
011	-24	-0.500
100	-60	-1.250
101	+6	+0.125
110	+12	+0.250
111	+24	+0.500



- High Driving Capability
 - 4 bumps per each COM output \rightarrow 180um pitch
 - Dual set of backplanes
 - Ro(BP) = 1 k Ω ; Ro(SEG) = 2.5 k Ω
 - Can drive big segments
 - Can be used in combination with high-res ITO \rightarrow low cost

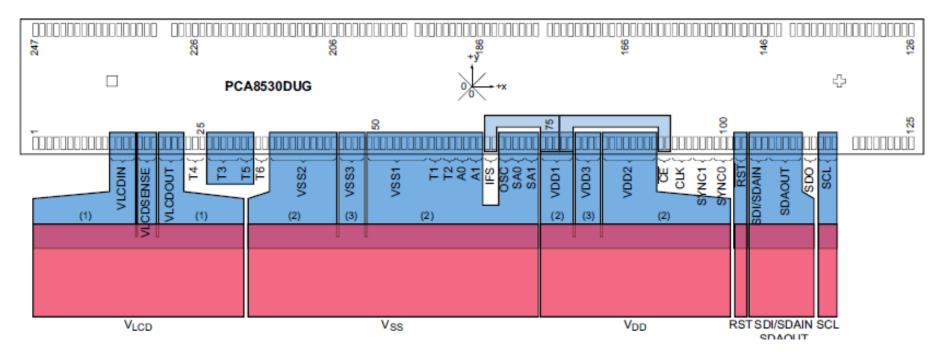




14 PUBLIC USE

High resistance to EMI

- Allow a wide VSS connection
- Fewer connections, fewer external components
- -EMC detection bits





High precision

- -VLCD accuracy: $\pm 60 \text{ mV}$ at 8 V, RT
- Frame frequency accuracy: ± 3 Hz at 80 Hz, RT

Highly featured

- . . .

- Programmable FF: 45Hz to 300Hz
- Programmable VLCD: 4V to 12V
- Diagnostic function
- -Temperature readout
- Multiple reset capabilities

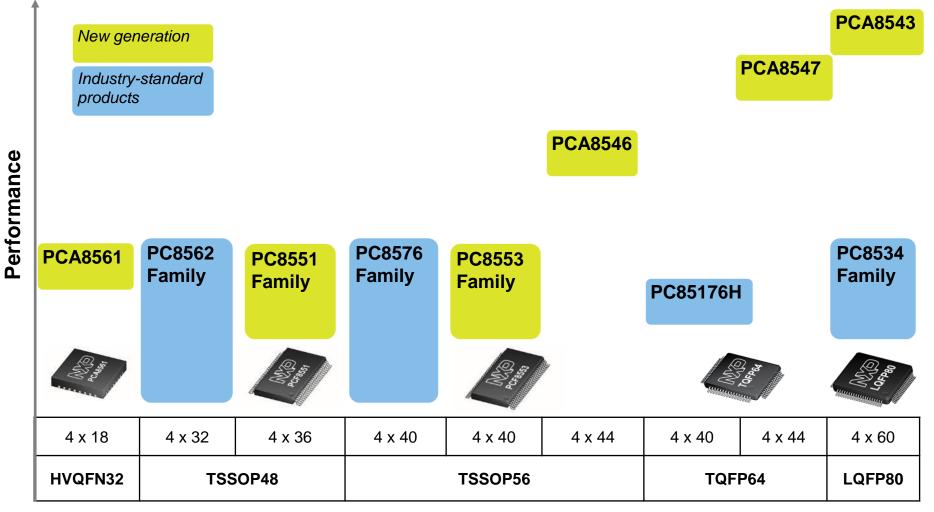
Bit	Symbol	Value	Description				
-	R/W	1	fixed value				
-	RS[1:0]	00	fixed value				
Temper	ature readout if SO = 0	(see Table 11)					
7 to 0	TD[7:0]	00000000 to 11111111	temperature readout (see <u>Section 8.10.4.1 on</u> page 40)				
Device	status readout if SO = 1	l (see <u>Table 11</u>)	-				
7	SR7		display status (see Table 22 on page 21)				
		0	display is disabled				
		1	display is enabled				
6	SR6		charge pump switching status (status of bit CPE, see <u>Table 14 on page 15</u>)				
		0	charge pump disabled				
		1	charge pump enabled				
5	SR5		charge pump charge status				
		0	charge pump has not reached programmed value				
		1	charge pump has reached programmed value				
4	SR4		reset status flag				
		0	no reset has occurred since the reset status flag was cleared last time				
		1	reset has occurred since the reset status flag was cleared last time ^[1]				
3 to 0	SR[3:0]		EMC detection				
		01SA1SA0	pre-defined code for EMC detection when I ² C interface is used				
		0101	pre-defined code for EMC detection when SP interface is used				



CASED LCD DRIVERS



Cased LCD Segment Drivers – Mux 1:4



Package and Resolution



18 PUBLIC USE FTF-AUT-N1918

PCA8561

4 x 18 LCD segment driver in HVQFN32 package

Small-footprint, high-quality, low-power, & low-cost

Main features	PCA8561					
Resolution (max)	4 x 18 = 72 segments					
VDD range [V]	1.8V ÷ 5.5V					
VLCD external [V]	1.8V ÷ 5.5V					
Mux rates	1:1, 1:2, 1:3, 1:4					
Bias configuration	static,1/2, 1/3					
Oscillator	Internal or external selectable through command					
Frame Freq.	32 Hz ÷ 256 Hz programmable					
Reset	Input reset pin (RST); software reset command; POR circuit with POR enable input pin (PORE)					
Interface	PCA8561AHN: 2-lines I ² C PCA8561BHN: 3-lines SPI					
Others	A0,A1 pins for I ² C slave address selection					
Operating Temp Range	-40°C to +105°C					
Package	HVQFN32 (with wettable flanks)					
ESD	HBM ± 3.5 kV					
Qualification	AEC-Q100 grade 2					

Released for production



A stand-alone LCD driver per each display avoid any routing and mechanical issues



The TSSOP package is to big to be mounted inside the knob of a climate control



Small passive displays in the clusters in combination with the big TFT display in the center. The MCU is advanced for TFT controller and does not encompass any LCD driver.



PCA8561 4 x 18 LCD segment driver in HVQFN32 package

Package Details HVQFN32 (DFN5050-32; outline: SOT617-3)



Parameter	Value
Width	5.0 mm
Length	5.0 mm
Height	0.85 mm
Pitch	0.5 mm
Soldering	wettable flanks



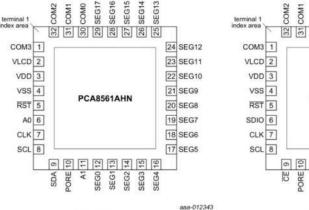
019eac652

100

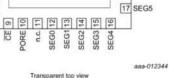
019886649

Wettable flanks:

The exposed edge of each terminal contains a small plated cavity, ensuring the solder flows into it and adheres to the side of the terminal. Non-wetting of the sides can be detected easily, allowing a cost-effective optical inspection process.



Transparent top view



PCA8561BHN

24 SEG12

23 SEG11

22 SEG10

21 SEG9

20 SEG8

19 SEG7

18 SEG6



20 PUBLIC USE FTF-AUT-N1918

PCA8561

4 x 18 LCD segment driver in HVQFN32 package

- Key applications
 - Car climate control unit
 - Small passive displays in a car instrument cluster
 - Consumer healthcare / battery operated devices
- Key benefits
 - -Reduce the PCB area
 - Sustain high operating temperatures
 - High EMC robustness
 - Easy visual inspection
 - Ultra low-power

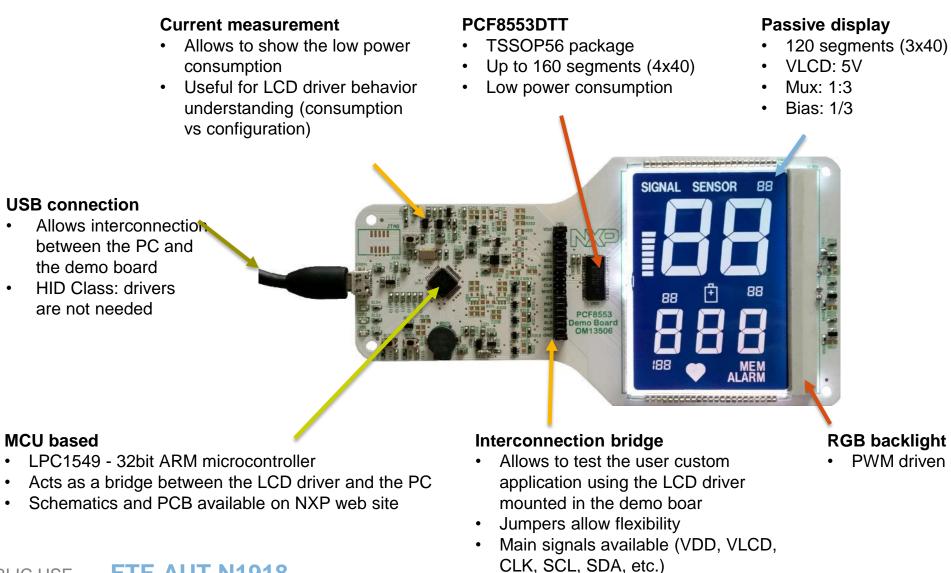








PCA8553DTT OM13506 Demo Board (Hardware)

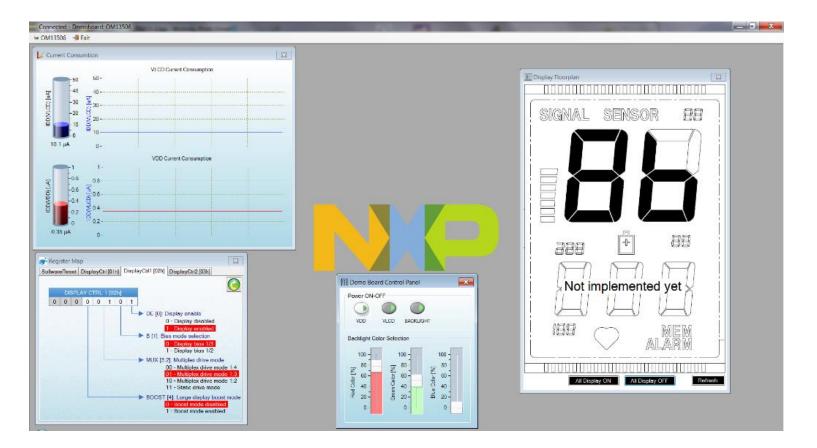




22 PUBLIC USE FTF-AUT-N1918

PCA8553DTT OM13506 Demo Board (Software)

- Graphical User Interface (GUI) for Windows
- User friendly
- Easy to use
- Plug & Play design
- Four sections available:
 - Current consumption
 - Display floorplan
 - Power supply control
 - -Register map





LED CONTROLLERS



NXP LED Controller for Automotive Focus Application Areas



Instrument Cluster



Climate Control



Car Radio

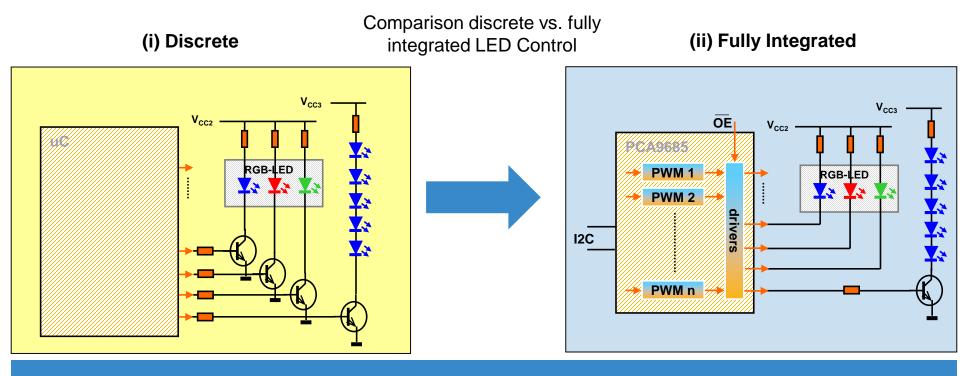


Navigation System (LCD Backlight & Control Switches)



NXP LED Controller for Automotive Value proposition (1/2)

Fully integrated LED Control with Color Mixing and Dimming



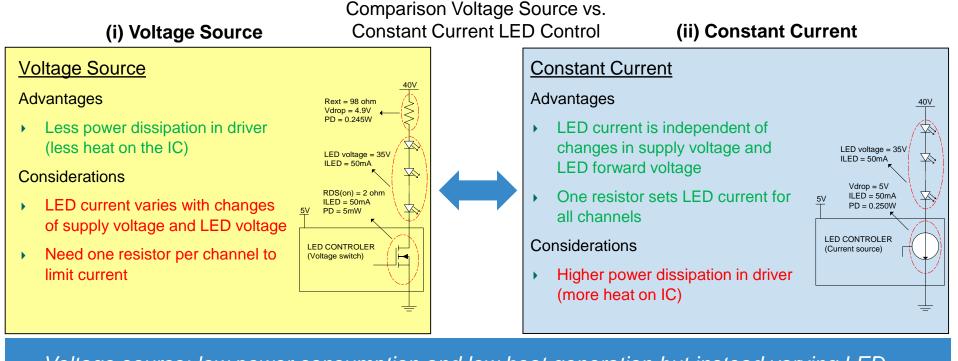
- Reduced Bill of Material by integration of up to 16-PWM channels into one single controller
- Add value by smart color mixing and global dimming capability



26 PUBLIC USE FTF-AUT-N1918

NXP LED Controller for Automotive Value proposition (2/2)

Voltage Source or Constant Current Devices



- Voltage source: low power consumption and low heat generation but instead varying LED brightness with varying supply voltage and need for external resistor
- Constant Current: higher power consumption and higher heat generation but instead constant
 LED brightness and no need for external resistors



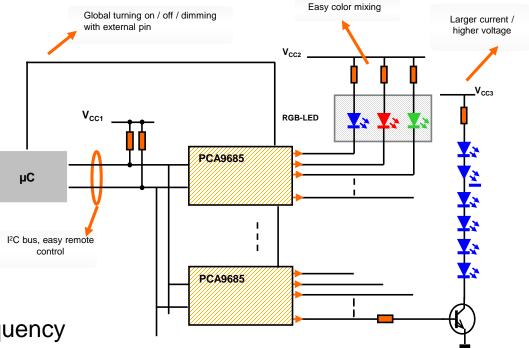
Voltage Source PWM LED Controller

➔ For cluster backlight, push button and tell tale illumination

- PCA9635PW/Q900
 - 16 + 1 PWM channels (16 individual, one global)
 - 8-bit PWM resolution (256 steps); 96kHz PWM frequency
 - 25mA output sink current; 5V compliant
 - -40°C, ..., +85°C; TSSOP28 package
 - AEC-Q100 automotive compliant qualification

• PCA9685PW/Q900

- 16 PWM channels, one global Output Enable
- 12-bit PWM resolution (4096 steps); 40Hz-1000Hz PWM frequency
- 25mA output sink current; 5V compliant
- -40°C, .. , +85°C; TSSOP28 package
- AEC-Q100 automotive compliant qualification



Automotive AEC-Q100 Qualified

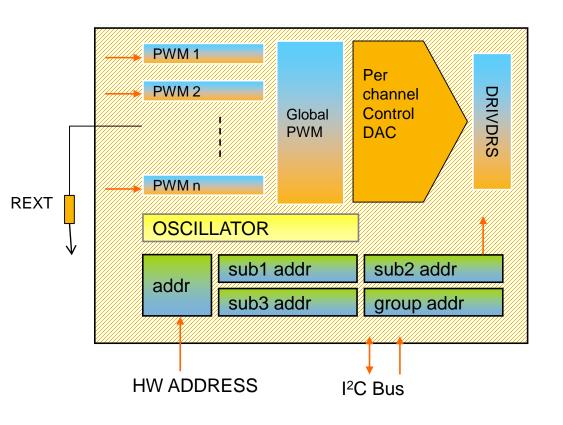


Constant Current PWM LED Controller

➔ For cluster backlight, push button and tell tale illumination

PCA9955BTW/Q900

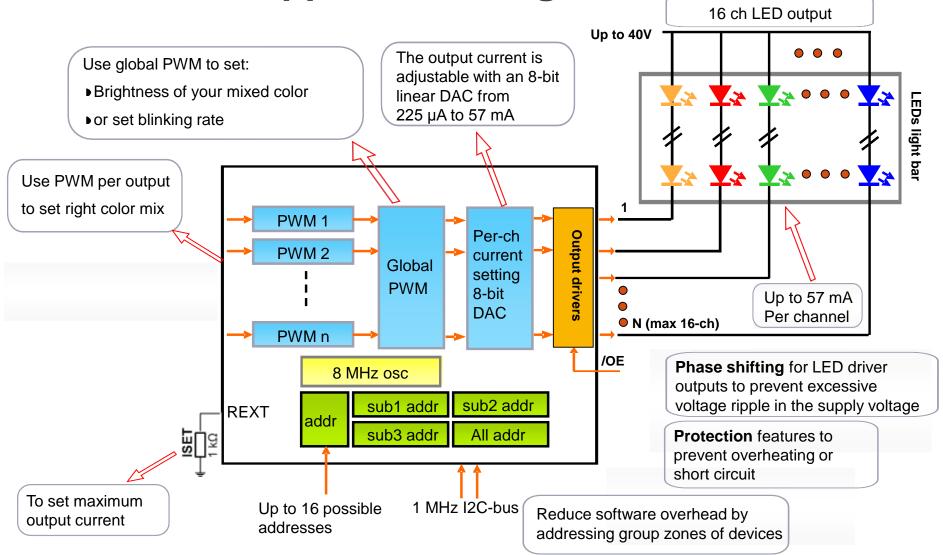
- 57-mA Constant current LED drivers @ 20V
- External resistor to set the overall output current
- Output to output accuracy + 4%
- Chip to chip output accuracy + 6%
- 8-bit programmable output to output delay
- Per channel 8-bit DAC to set individual output current
- Per channel 8-bit PWM to dim LED in 256 steps
- Global 8-bit PWM to dim or blink in 256 steps
- Gradation control for LED output for up to 4 zones
- 3 HW ADDR pins to allow up to 125 devices per bus
- 4 programmable sub calls address groups for cluster control
- LED open/short, over-temp, over current detection
- Fast-mode Plus (1 MHz) I2C-bus interface
- Thermally enhanced package HTSSOP28
- AEC-Q100 automotive compliant qualification
- 29 PUBLIC USE **FTF-AUT-N1918**



Automotive AEC-Q100 Qualified

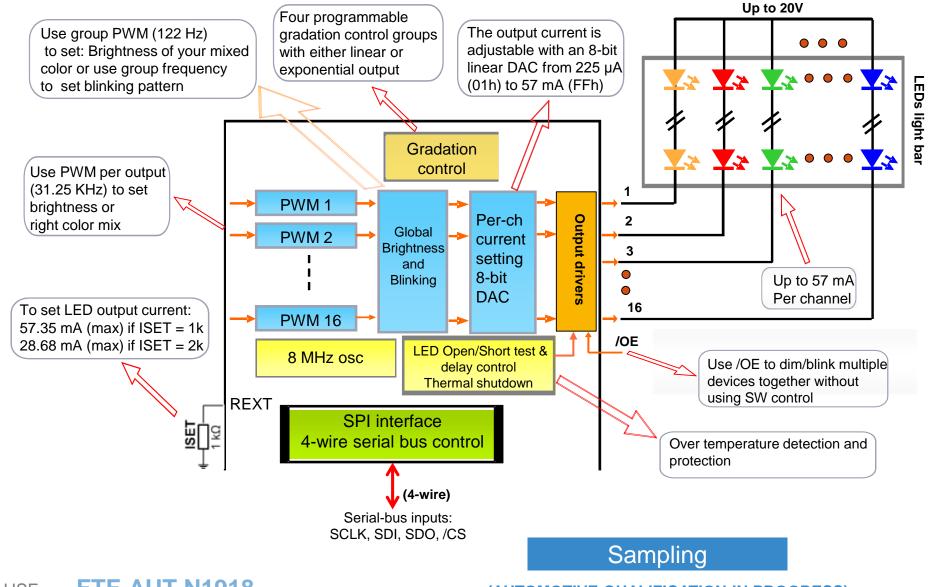


PCA9955BTW/Q900 Application Diagram

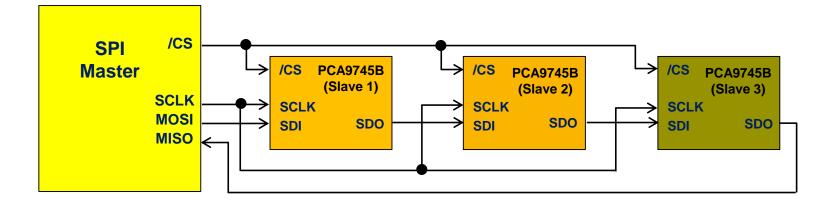




PCA9745B SPI Daisy-chain LED Driver Block Diagram



PCA9745B SPI Daisy-chain Connection



SPI Data Format

D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
NSB	Register Address				LSB	R/W	MSB	Data					LSB		

- SPI Master send 16-bit (clocks with address and data) x 3 (number of slaves) to access all slave devices at the same time
- Only one byte data can be read/write from/to all slave devices
- No slave address required





SUMMARY



NXP Secure Interface & Power Solutions Automotive Qualified Devices & Targeted Devices

- Continuously expanding automotive portfolio by post qualification of existing products
- Portfolio of more than 700 products and more than 10'000 customers worldwide
- More than 25 years experience in delivering into automotive industry



34 PUBLIC USE FTF-AUT-N1918

Conclusion & Summary

 For more information about NXP LED and LCD products, as well as the entire NXP I²C product portfolio, visit <u>www.nxp.com/i2c</u>





SECURE CONNECTIONS FOR A SMARTER WORLD

ATTRIBUTION STATEMENT

NXP, the NXP logo, NXP SECURE CONNECTIONS FOR A SMARTER WORLD, CoolFlux, EMBRACE, GREENCHIP, HITAG, I2C BUS, ICODE, JCOP, LIFE VIBES, MIFARE, MIFARE, MIFARE Classic, MIFARE DESFire, MIFARE Plus, MIFARE FleX, MANTIS, MIFARE ULTRALIGHT, MIFARE4MOBILE, MIGLO, NTAG, ROADLINK, SMARTLX, SMARTMX, STARPLUG, TOPFET, TrenchMOS, UCODE, Freescale, the Freescale logo, AltiVec, C 5, CodeTEST, CodeWarrior, ColdFire, ColdFire+, C Ware, the Energy Efficient Solutions logo, Kinetis, Layerscape, MagniV, mobileGT, PEG, PowerQUICC, Processor Expert, QorIQ, QorIQ Qonverge, Ready Play, SafeAssure, the SafeAssure logo, StarCore, Symphony, VortiQa, Vybrid, Airfast, BeeKit, BeeStack, CoreNet, Flexis, MXC, Platform in a Package, QUICC Engine, SMARTMOS, Tower, TurboLink, and UMEMS are trademarks of NXP B.V. All other product or service names are the property of their respective owners. ARM, AMBA, ARM Powered, Artisan, Cortex, Jazelle, Keil, SecurCore, Thumb, TrustZone, and µVision are registered trademarks of ARM Limited (or its subsidiaries) in the EU and/or elsewhere. ARM7, ARM9, ARM11, big.LITTLE, CoreLink, CoreSight, DesignStart, Mali, mbed, NEON, POP, Sensinode, Socrates, ULINK and Versatile are trademarks of ARM Limited (or its subsidiaries) in the EU and/or elsewhere. All rights reserved. Oracle and Java are registered trademarks of Oracle and/or its affiliates. The Power Architecture and Power.org word marks and the Power and Power.org logos and related marks are trademarks and service marks licensed by Power.org. © 2015–2016 NXP B.V.