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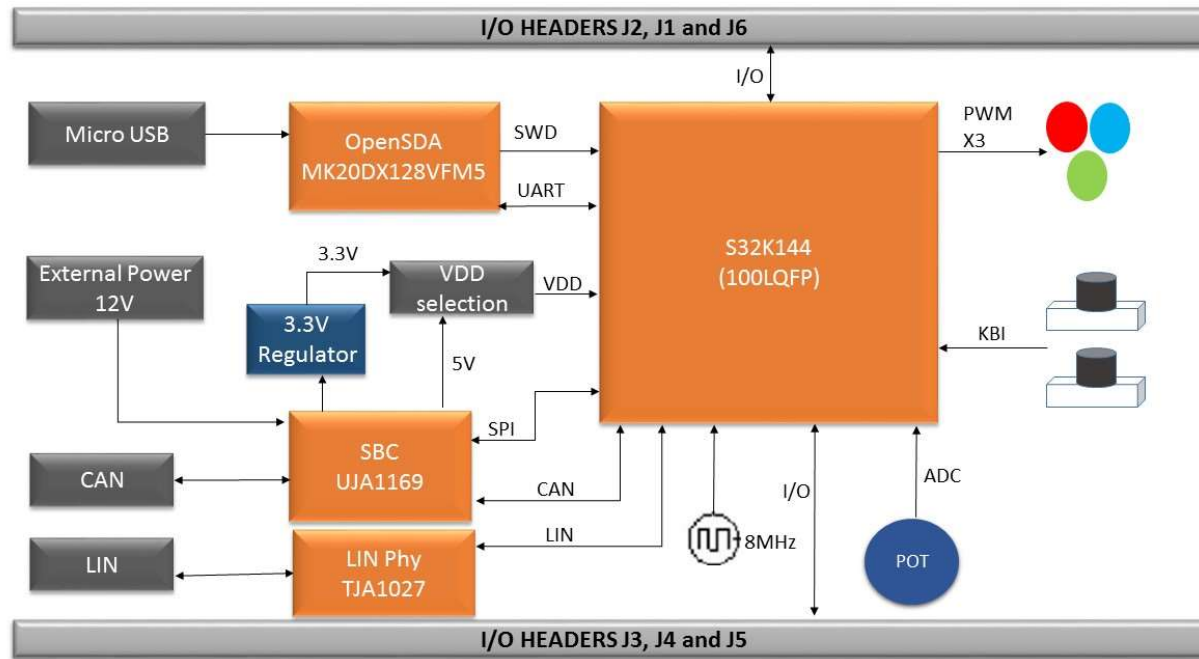
Revisions

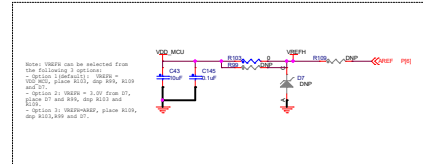
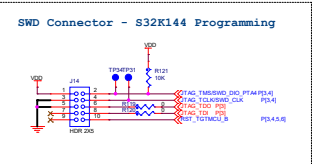
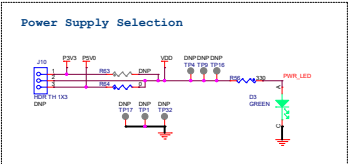
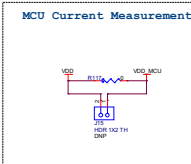
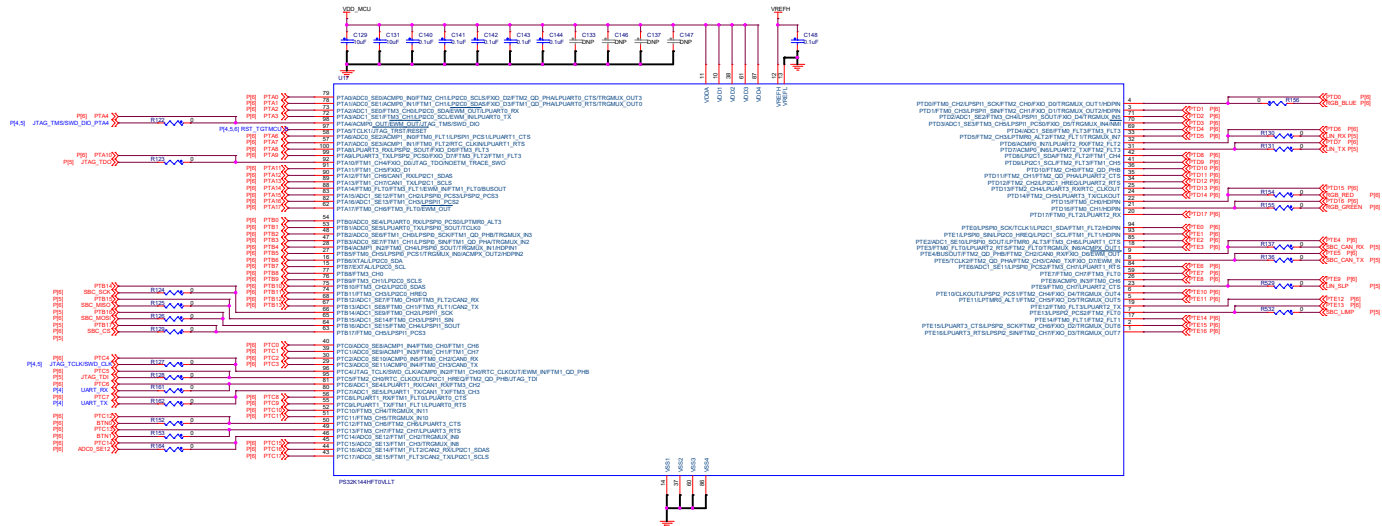
Rev	Description	Date	Approved
XA	Initial Release	APR-13-2016	O. Romero
A	Prototype Production	APR-14-2016	O. Romero
AX1	Development	AUG-16-2016	O. Romero
B	2nd Release	SEP-02-2016	O. Romero
B1	Update BCM	DEC-16-2016	O. Romero
C	Update BCM	MAR-1-2018	A. Gonzalez

S32K144EVB-Q100

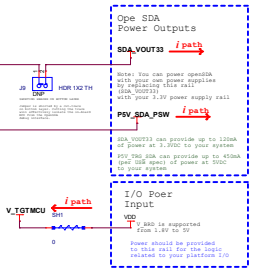
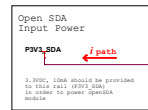
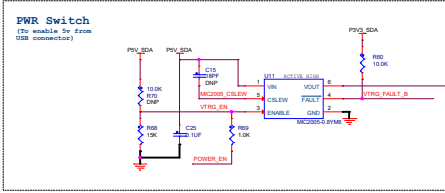
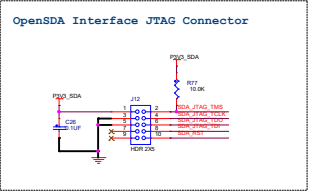
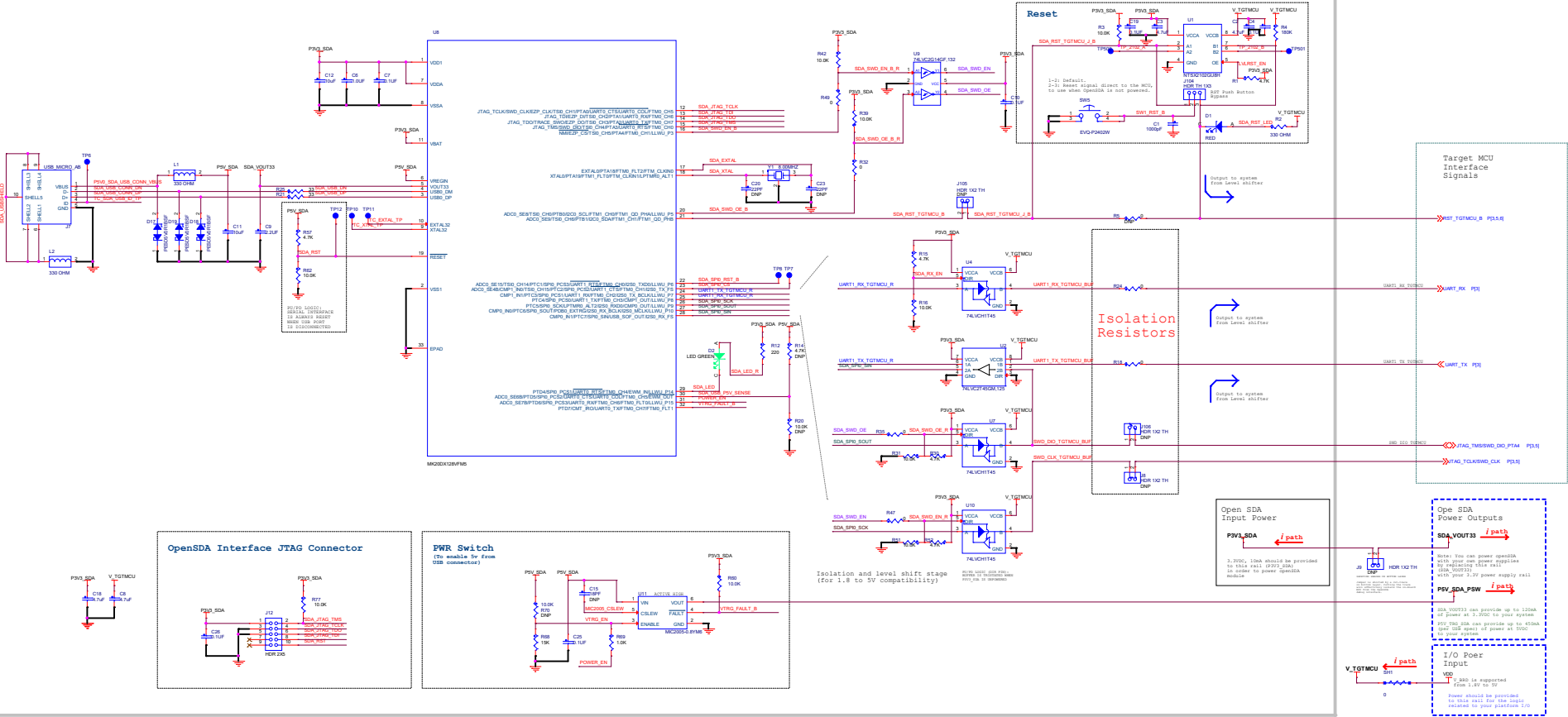
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Designer: Oswaldo Romero	Drawing Title: S32K144EVB-Q100		
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Approved: APPROVER	Size C	Document Number SCH-29248 PDF: SFF-29248	Rev C
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- Unless Otherwise Specified:
 All resistors are in ohms, 1% and 5 %
 All capacitors are in uF, 10% , 20 % and 5%
 All voltages are DC
 All polarized capacitors are aluminum electrolytic
- Interrupted lines coded with the same letter or letter combinations are electrically connected.
- Device type number is for reference only. The number varies with the manufacturer.
- Special signal usage:
 _B Denotes - Active-Low Signal
 <> or [] Denotes - Vectored Signals
- Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

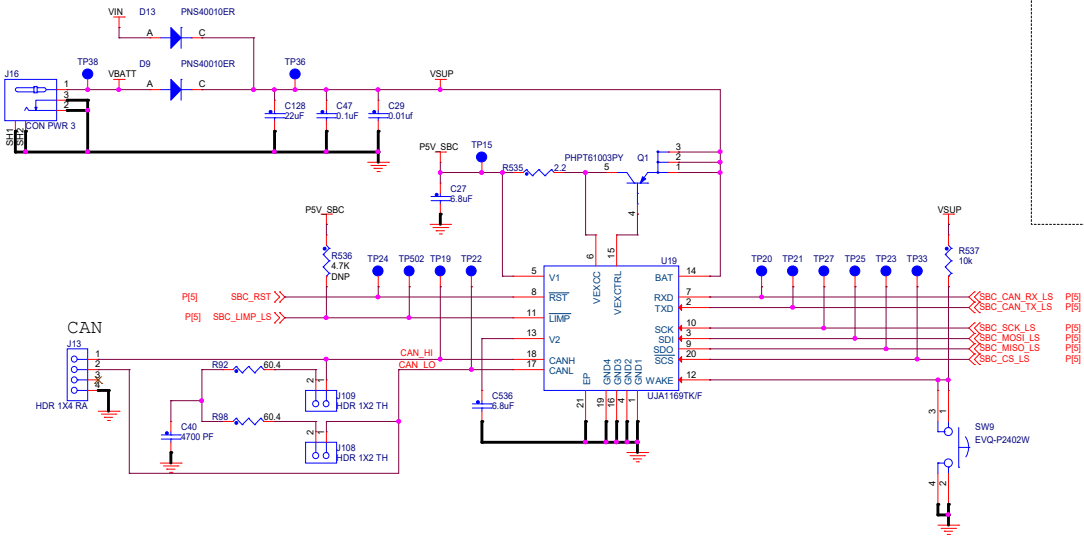




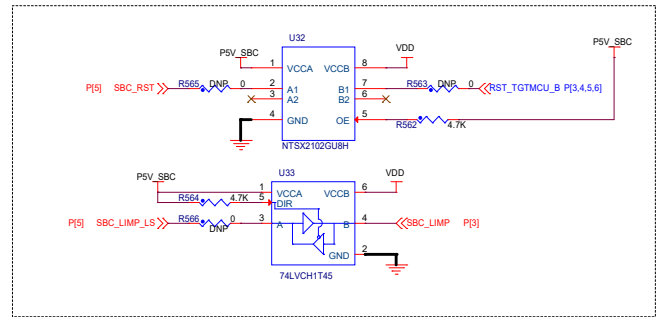
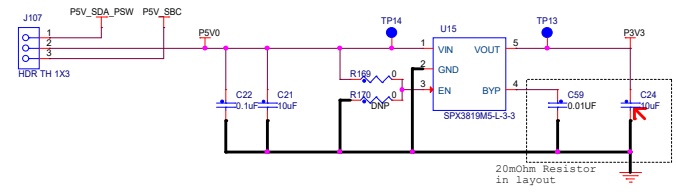
OpenSDA Interface



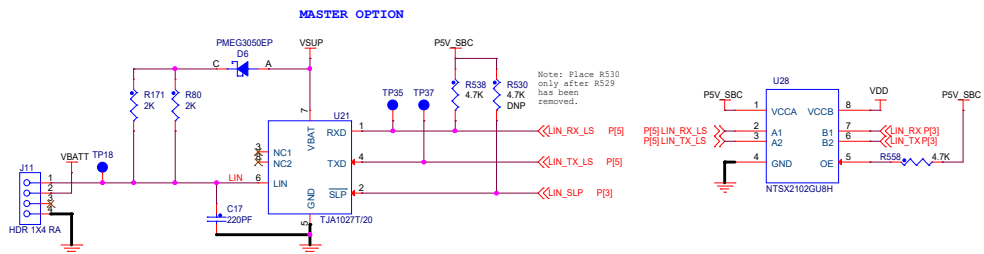
CAN Physical Layer



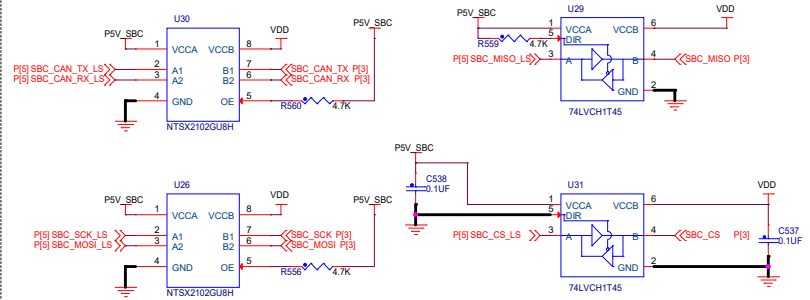
3.3V LDO Power Supply

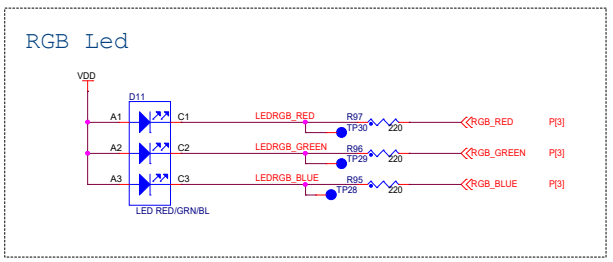
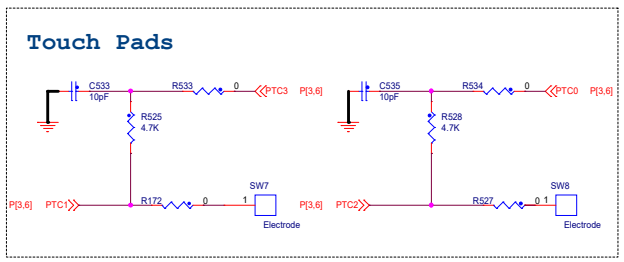
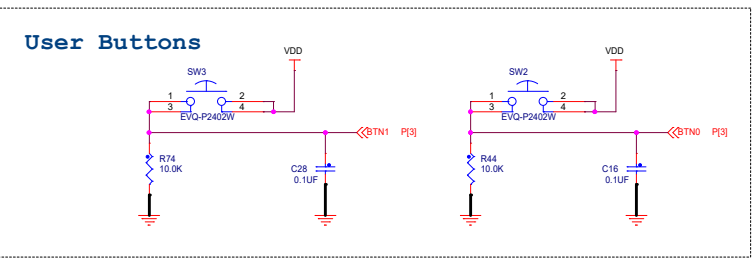
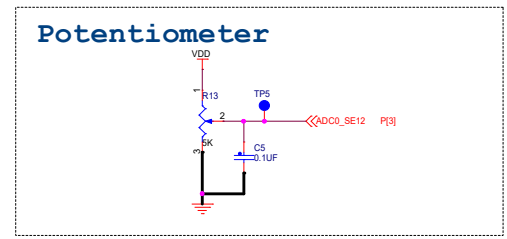
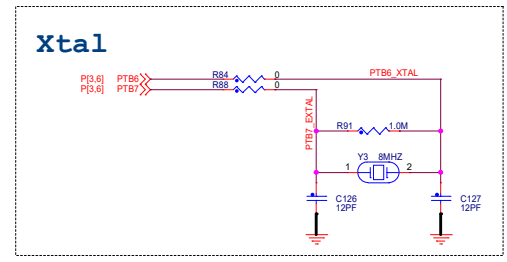
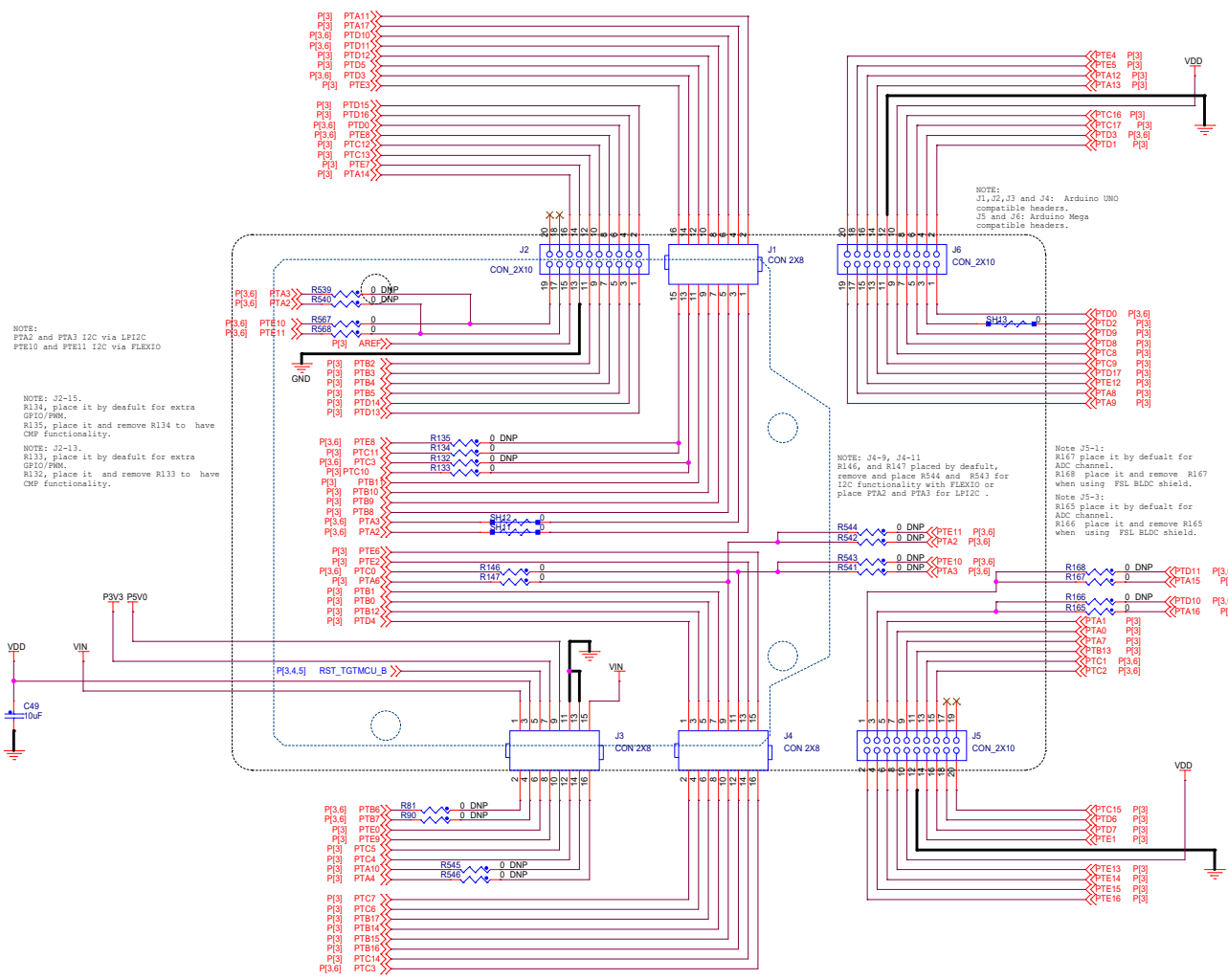


LIN Physical Layer



Level Shifters





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S32K144EVb-Q100			
IO Headers			
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