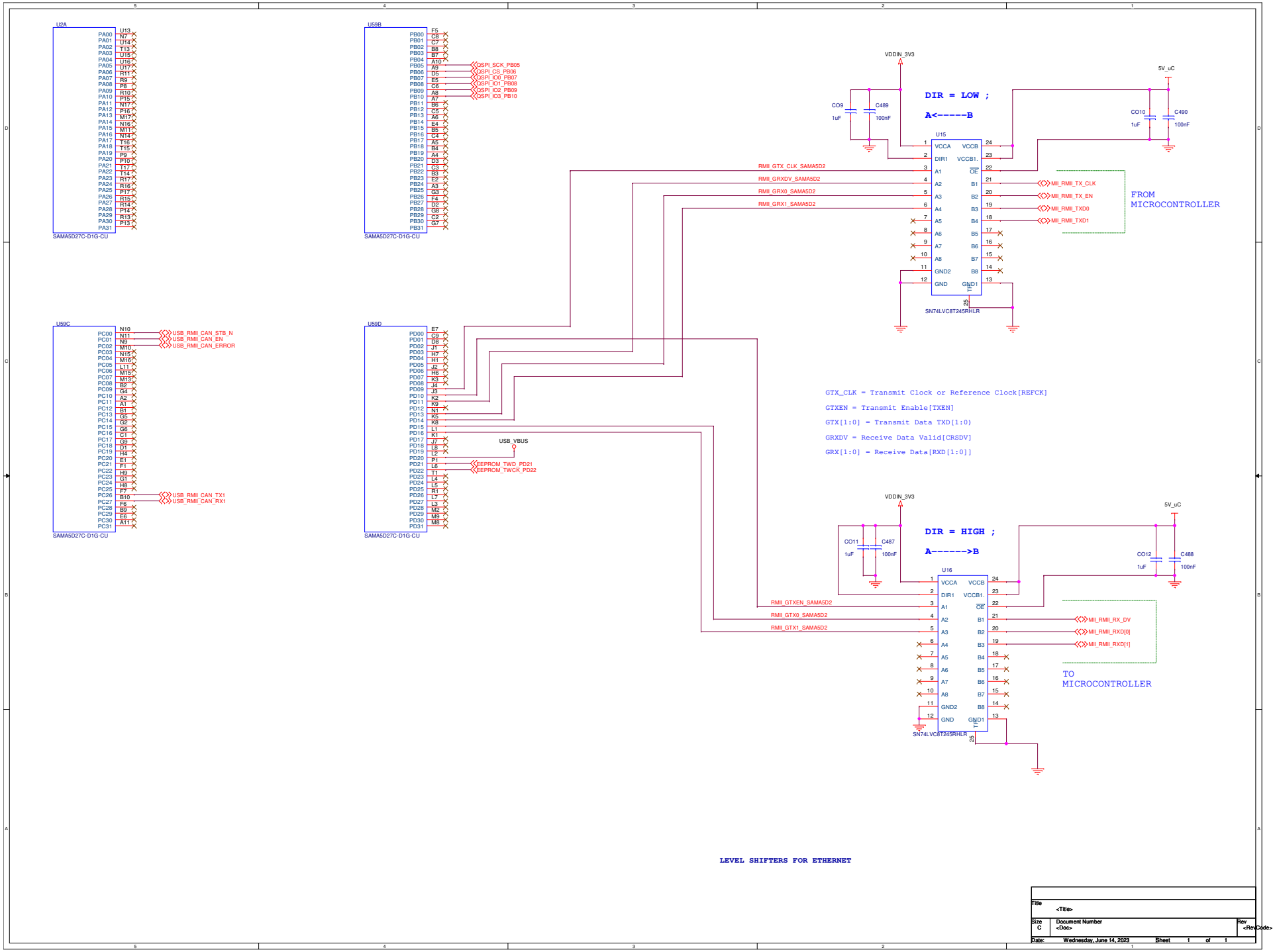
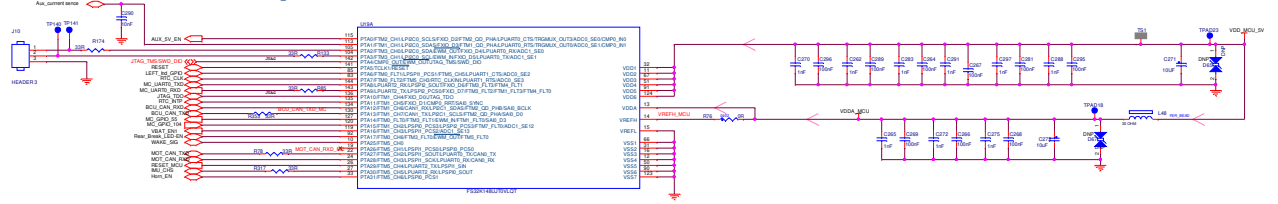
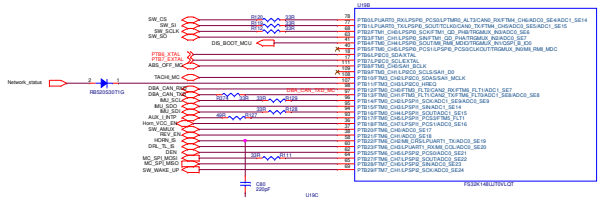
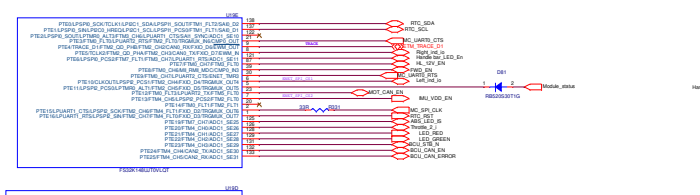
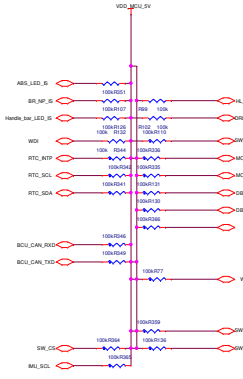

U2A
SAMA5D27C-D1G-CU
U59B
SAMA5D27C-D1G-CU
QSPI_SCK_PB05
QSPI_CS_PB06
QSPI_IO0_PB07
QSPI_IO1_PB08
QSPI_IO2_PB09
QSPI_IO3_PB10
U59C
SAMA5D27C-D1G-CU
USB_RMII_CAN_STB_N
USB_RMII_CAN_EN
USB_RMII_CAN_ERROR
USB_RMII_CAN_TX1
USB_RMII_CAN_RX1
U59D
SAMA5D27C-D1G-CU
USB_VBUS
EEPROM_TWD_PD21
EEPROM_TWCK_PD22
VDDIN_3V3
5V_uC
DIR = LOW ;
A<-----B
U15
SN74LVC8T245RHLR
RMII_GTX_CLK_SAMA5D2
RMII_GRXDV_SAMA5D2
RMII_GRX0_SAMA5D2
RMII_GRX1_SAMA5D2
MII_RMII_TX_CLK
MII_RMII_TX_EN
MII_RMII_TXD0
MII_RMII_TXD1
FROM
MICROCONTROLLER
GTX_CLK = Transmit Clock or Reference Clock[REFCK]
GTXEN = Transmit Enable[TXEN]
GTX[1:0] = Transmit Data TXD[1:0]
GRXDV = Receive Data Valid[CRSDV]
GRX[1:0] = Receive Data[RXD[1:0]]
DIR = HIGH ;
A------>B
U16
SN74LVC8T245RHLR
RMII_GTXEN_SAMA5D2
RMII_GTX0_SAMA5D2
RMII_GTX1_SAMA5D2
MII_RMII_RX_DV
MII_RMII_RXD[0]
MII_RMII_RXD[1]
TO
MICROCONTROLLER
LEVEL SHIFTERS FOR ETHERNET
Title
<Title>
Size
C
Document Number
<Doc>
Rev
<RevCode>
Date:
Wednesday, June 14, 2023
Sheet
1
of
1

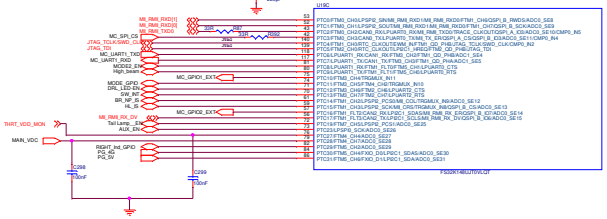
S32K148 Microcontroller
144pins LQFP

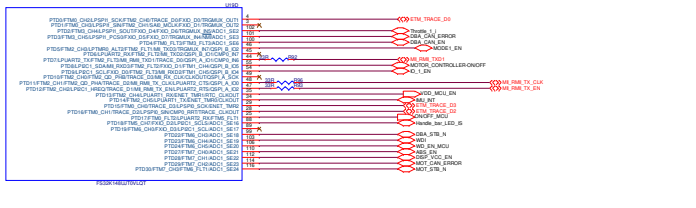




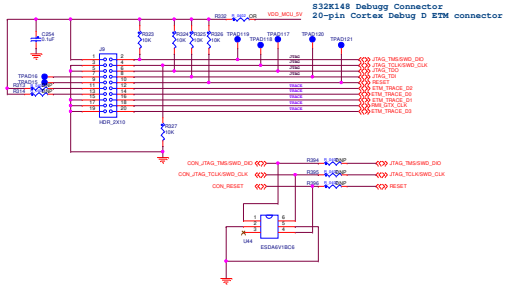


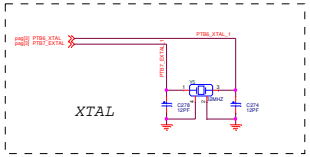


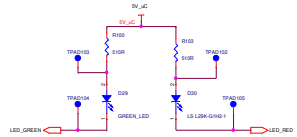




S32K148 Debugg Connector
20-pin Cortex Debug D ETM connector







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