1、make can_routing

> PS D:\Bootloader-llce1.0.4\S32G_LLCE_1_0_4\sample_app_llce\llce_sample_app_af> make clean > PS D:\Bootloader-llce1.0.4\S32G_LLCE_1_0_4\sample_app_llce\llce_sample_app_af> make can_routing

2、use arm-none-eabi-objcopy.exe, create a can_routing.bin

reserver.com/states/interverse

3、create IVT image,create blob image ,then flash to QSPI.

Looking for the map file of the project, the SRAM address, and vector_table address are both 0x34000000.

Boot Configuration	0	起始地址 0x120	~ 大小 (5	2节) 4			
Boot Target M7_0 *		HSE FW Configuration					
BOOTSEQ: Secured boot mode		Reserved		HSE (backup)	8		
Boot Target Watchdog		N/A					
GMAC Generation		起始地址 0x128	~ 大小 (名				
Key Type Plain ADKP 👻	8						
Use new authenticated image format (only for S32G2 Rev2.1 a	nd above)	On			8		
Key File N/A	Application bootloader						
Life Cycle	D:\Bootloader-llce1.0.4\S32G_LLCE_1_0_4\sample_app_llce\llce_sample_app_af\tmp\can_routing.bin						
Life Cycle Keep existing configuration	超始地址 0x400						
Interface selection]	 Application Boot Code Imag 	e				
Boot device type QuadSPI Serial Flash 👻		 Application Boot Image 					
Configure QuadSPI parameters							
				RAM start pointer			
QuadSPI p N/A		Address 0x34000000		SRAM_address			
IVT 镜像地址				RAM entry pointer	- I II		
IVT 镜像起始地址 0x0 🗘				Kaw entry pointer			
		Address 0x34000000		vector_table_address			
自动调整起始地址: 0x100	调整			1			
		Code length 1102696					
导入 IVT 镜像 导出 IVT 镜像		 Serial Boot 			=		
49-L1 1V 1 192108		[[unant lunant					
导出 Blob 镜像 2		1					
导出 Blob 镜像		Reserved			8		
闪存镜像 3				Application bootloader (backup)			
∽s326_LLCE_1_0_4 □ □ □ □ □	121 LUA	ບ ແmp/obj/gcc/can_rout	ing/can_4	3_LLCE_1PW.0			
> .vscode		D tmp/obj/gcc/can_rout					
> firmware		D tmp/obj/gcc/can_rout D tmp/obj/gcc/can_rout					
> plugins		AD tmp/obj/gcc/can_routing/Lice_InterfaceCanConfig.o					
✓ sample_app_lice		AD tmp/obj/gcc/can_routing/Llce_SwFifo.o					
 ✓ IIce_sample_app_af > can_common 		AD C:\Users\HEJINR~1\AppData\Local\Temp\cca2B6x1.o AD C:\Users\HEJINR~1\AppData\Local\Temp\ccEpCQYu.o					
> platform_common		RT GROUP					
 sample_app_can_customization 		AD d:/nxp/s32ds.3.5/s32ds/build_tools/gcc_v9.2/gcc-9.2-arm32-eabi/bin//lib/gcc/arm-nor					
✓ sample_app_can_llce		ND d:/nxp/s32ds.3.5/s32ds/build_tools/gcc_v9.2/gcc-9.2-arm32-eabi/bin//arm-none-eabi/r ND d:/nxp/s32ds.3.5/s32ds/build_tools/gcc_v9.2/gcc-9.2-arm32-eabi/bin//arm-none-eabi/r					
✓ can_mcal_can2can∖src		GROUP	us/builu_		-eaut/n		
C main.c > can_mcal_loopback							
> can_mcal_multihost	135 .al		00 0x14	4e8			
✓ tresos_s32g2	136 *(137	.core_loop) 0x3400000	10	. = ALIGN (0x4)			
> .metadata		.exception_table)					
> Tresos_CAN2CAN_Project		.intc vector)					
> Tresos_LoopBack_Project	140 .i 141	ntc_vector 0x3400000 0x3400000		408 C:\Users\HEJINR~1\AppData\Local\Temp\ccEpCQYu. VTABLE	.0		
> Tresos_Multihost_Project > tresos_s32g3		.startup)		TABLE			
 sample_app_lin_lice 		tartup 0x3400040		<pre>kbc C:\Users\HEJINR~1\AppData\Local\Temp\cca2B6x1.</pre>	.0		
✓ tmp	144	0x3400040		_start			
> obj	145 146 *(0x3400040 .systeminit)	0	Reset_Handler			
M .map		ysteminit 0x340004d		0x6 tmp/obj/gcc/can_routing/exceptions.o			
≣ build.log	148	0x340004	4	NMI Handler			

4、flash successful.

器闪存	寬慶			×
镜像:	D:\Bootloader-llce1.0.4\S32G_LLCE_1_0_4\sample_app_llce\llce_sample_app_af\tmp\can_routing_blob.bin		浏览	
算法:	MX25UW51245G.bin	~	添加	
串口:	сомя			~
已完成				
	cation successful. file is loaded.			^
- Time	spent: 23.11 sec.			
已成功第	石以:			¥
	取消		闪存	