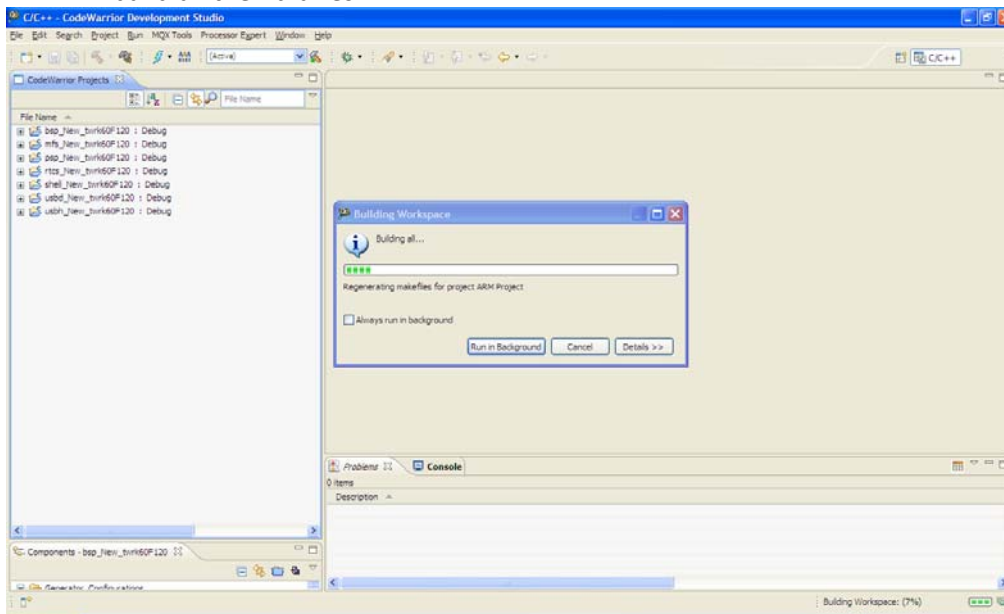


In order to reproduce your issue I followed your steps:

1. First I cloned the BSP



2. I build all the libraries.



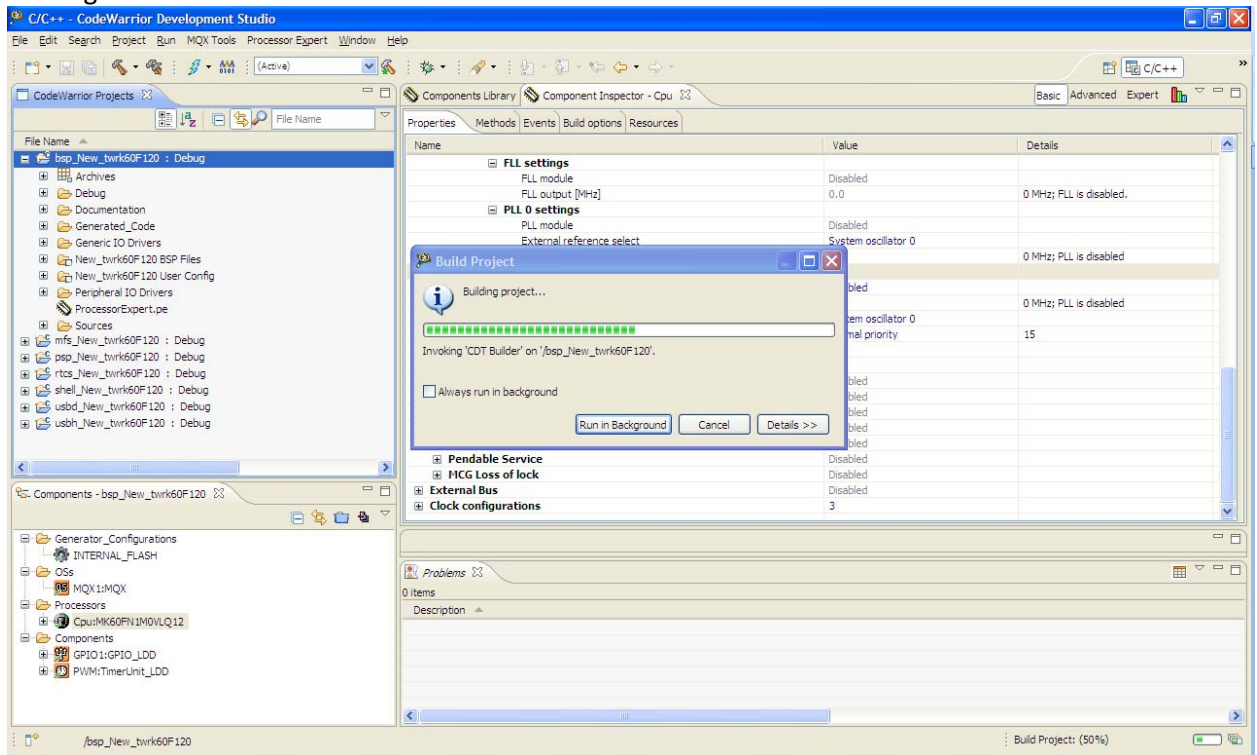
3. I modified the clock settings like you mention

Name	Value	Details
CPU type	MK60FN1M0VLQ12	
Clock settings		
Internal oscillator		
Slow internal reference clock [kHz]	32.768	32.768 kHz
Fast internal reference clock [MHz]	4.0	4 MHz
RTC oscillator	Enabled	
Clock frequency [MHz]	0.032768	0.032768 MHz
System oscillator 0	Enabled	
Clock source	External reference clock	
Clock frequency [MHz]	12.0	12 MHz
System oscillator 1	Disabled	
Clock source settings		
Clock source setting 0		
MCG settings		
MCG mode	PEE	
PLL select	PLL 0	
MCG output [MHz]	120.0	120 MHz
MCG external ref. clock source	System oscillator 0	
MCG external ref. clock [MHz]	12.0	12 MHz
FLL settings		
FLL module	Disabled	
FLL output [MHz]	0.0	0 MHz; FLL is disabled.
PLL 0 settings		

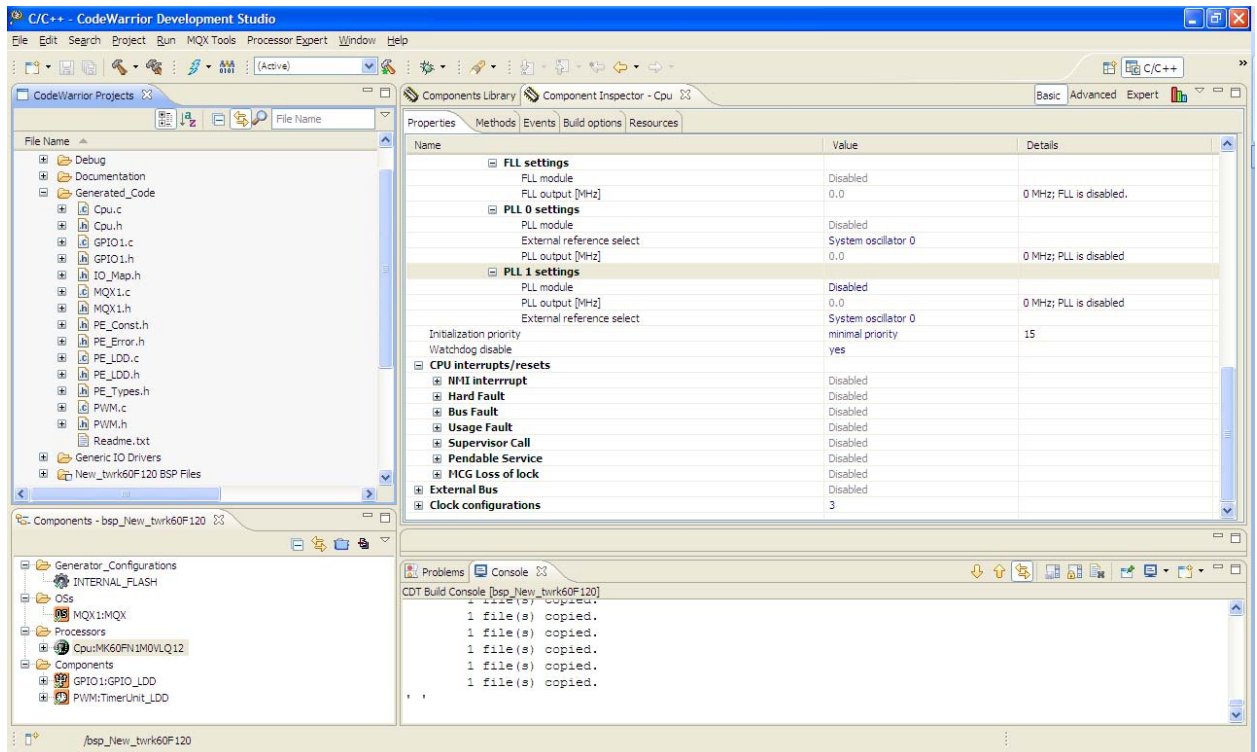
4. Generated code

The screenshot shows the CodeWarrior Development Studio interface. The Component Inspector window is open, displaying the same CPU settings as the table above. A 'Generating code' dialog box is overlaid on the Component Inspector, indicating that code is being generated for the processor module. The dialog box has a progress bar and buttons for 'Run in Background', 'Cancel', and 'Details >>'. The Component Inspector window shows the 'MCG external ref. clock [MHz]' setting highlighted in yellow, with a value of 12.0 and a detail of 12 MHz.

5. Build again the BSP



6. At this point I can build the BSP without errors. Unfortunately I cannot to reproduce your issue.



Could you please try cloning again the BSP, please follow the next steps:

1. Navigate to C:\Freescale\Freescale MQX 4.0\config\ TWR-K60F120M and drag TWR-K60F120M.wsd to the CodeWarrior
2. Build all the libraries
3. Clone again the TWR-K60F120M BSP
4. Modify the clock settings
5. generate code
6. Rebuild the BSP with new clock settings.

Please let me know the result of this test. Have a nice day!