NVIC_SetPriority **PEx BAD**

```
#if CPU_COMPONENTS_INIT
void Components_Init(void)
{

  /*! gpio1 Auto initialization start */
  GPIO_DRV_Init(gpio1_InpConfig1,gpio1_OutConfig1);
  /*! gpio1 Auto initialization end */
   /*! uartCom0 Auto initialization start */
   OSA_InstallIntHandler(UART0_RX_TX_IRQn, UART0_VP12_IRQHandler);
   UART_DRV_Init(FSL_UARTCOM0,&uartCom0_State,&uartCom0_InitConfig0);
   UART_DRV_InstallRxCallback(FSL_UARTCOM0, uartCom0_RxCallback, uart0RxBuff, NULL, true);
   NVIC_SetPriority(UART0_RX_TX_IRQn, 64U);
   /*! uartCom0 with DMA Auto initialization end */
   /*! uartCom1 Auto initialization start */
   OSA_InstallIntHandler(UART1_RX_TX_IRQn, UART1_VP12_IRQHandler);
   UART_DRV_Init(FSL_UARTCOM1,&uartCom1_State,&uartCom1_InitConfig0);
   UART_DRV_InstallRxCallback(FSL_UARTCOM1, uartCom1_RxCallback, uart1RxBuff, NULL, true);
   NVIC_SetPriority(UART1_RX_TX_IRQn, 64U);
   /*! uartCom1 with DMA Auto initialization end */
  /*! i2cCom0 Auto initialization start */
  OSA_InstallIntHandler(I2C0_IRQn, i2cCom0_IRQHandler);
  I2C_DRV_MasterInit(FSL_I2CCOM0, &i2cCom0_MasterState);
  I2C_DRV_MasterSetBaudRate(FSL_I2CCOM0, &i2cCom0_MasterConfig0);
  NVIC_SetPriority(I2C0_IRQn, 48U);
  /*! i2cCom0 Auto initialization end */
  /*! i2cCom2 Auto initialization start */
  OSA_InstallIntHandler(I2C2_IRQn, i2cCom2_IRQHandler);
  I2C_DRV_MasterInit(FSL_I2CCOM2, &i2cCom2_MasterState);
  I2C_DRV_MasterSetBaudRate(FSL_I2CCOM2, &i2cCom2_MasterConfig0);
  NVIC_SetPriority(I2C2_IRQn, 48U);
  /*! i2cCom2 Auto initialization end */
  /* ### XFormat "XF1" init code ... */
}
#endif /* CPU_COMPONENTS_INIT */
```