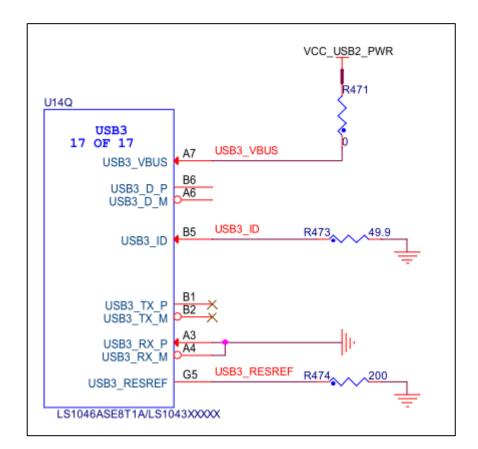
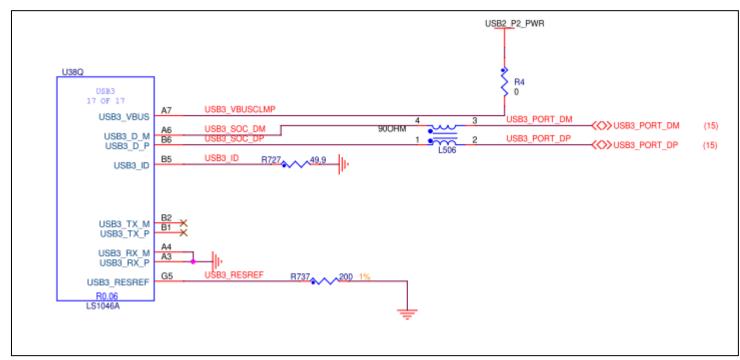
About the USB controller of LS1046A

Reference Board





FRWYLS1046A-PA

LS1046ARDB-PB

[LS1046ARDB-PB]

When not using the USB controller, connect as follows.

- ① USB3_VBUS is connected to a 5V power supply.
- 2 USB_ID is connected to GND with 49.9 Ω .

[FRWYLS1046A-PA]

When using only USB 2.0, the connection is as follows.

- ① USB3_VBUS is connected to a 5V power supply.

QorlQ LS1046A Design Checklist

5.12 USB PHY pin termination recommendations

Table 22. USB 1/2/3 PHY pin termination checklist

Signal Name	IO type	Used	Not Used	Completed
USB[1/2/3]_D_P	Ю	USB PHY Data Plus	Do not connect. These pins should be left floating.	
USB[1/2/3]_D_M	Ю	USB PHY Data Minus	Do not connect. These pins should be left floating.	
USB[1/2/3]_VBUS	I	USB1 power supply pin. A charge pump external to the USB 3.0 PHY must provide power to this pin. The nominal voltage for this pin is 5 V.	Do not connect. These pins should be left floating.	
USB[1/2/3]_ID	ı	USB PHY ID Detect	Pull low through a $1k\Omega$ resistor to GND.	
USB[1/2/3]_TX_P	0	USB PHY 3.0 Transmit Data (positive)	Do not connect. These pins should be left floating.	
USB[1/2/3]_TX_M	0	USB PHY 3.0 Transmit Data (negative)	Do not connect. These pins should be left floating.	
USB[1/2/3]_RX_P	ı	USB PHY 3.0 Receive Data (positive)	Connect to ground (GND)	
USB[1/2/3]_RX_M	ı	USB PHY 3.0 Receive Data (negative)	Connect to ground (GND)	
USB[1/2/3]_RESREF	Ю	Attach a 200-Ω 1% 100-ppm/ ⁰ C precision resistor-to-ground on the board.	Do not connect. These pins should be left floating.	

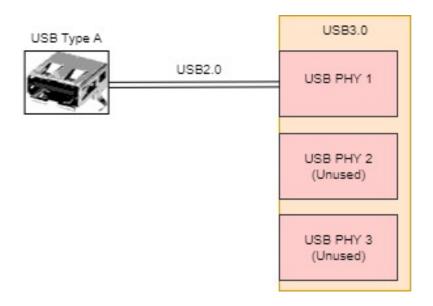
[Application Note]

AN5252.pdf (QorIQ LS1046A Design Checklist, Rev. 2, 06/2020)

Question

[Conditions]

- · USB1 works with USB2.0.
- · USB2 / 3 is unused.



[Check]

- 1. Does USB [2/3] _VBUS need a 5V power supply?
- 2. Is the USB [2/3] $_{\rm ID}$ 1k Ω connection correct?
- 3. Does USB [1] _RESREF require 200 Ω ?
- 4. Does USB [2/3] _RESREF require 200 Ω ?