20.4.8   GPIO Input Buffer Enable Register (GPIBE)
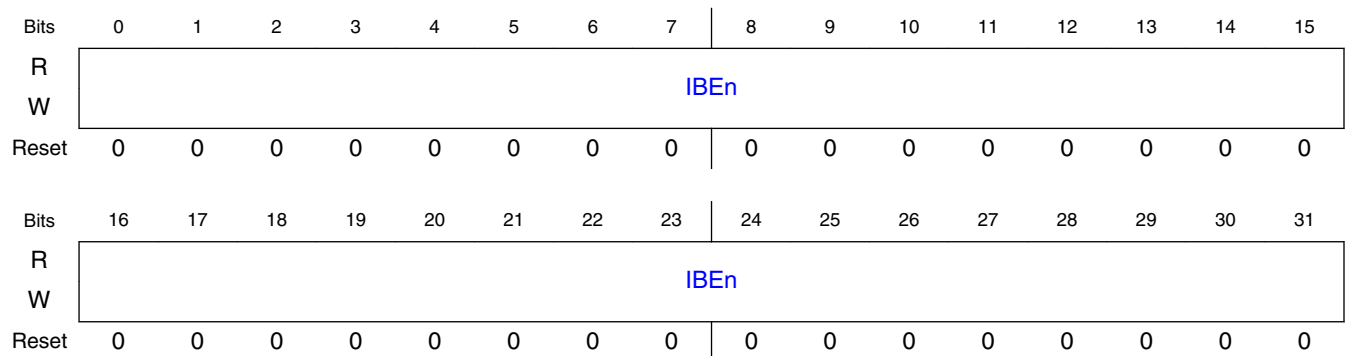
20.4.8.1   Offset

Register	Offset
GPIBE	18h

20.4.8.2   Function

The GPIO Input Buffer Enable register is used to control the input enable of each individual GPIO port. When an individual GPIO port's direction is set to input (GPIO_GPDIR[DRn=0]), the associated input enable must be set (GPIOxGPIE[IEn]=1) to propagate the port value to the GPIO Data Register. When a port's input enable is disabled, the port value does not propagate to the GPIO Data Register. Unused GPIO ports may be non-connected on the board if their "direction" and "input enable" are configured for "input" (GPIO_GPDIR[DRn=0]) and "disabled" (GPIBE[IEn=0]) respectively, which is the default configuration (based on GPIO input signal GPIBE_RESET_VAL[31:0] tied to 0x0000_0000 which sets the reset value of this register).

20.4.8.3   Diagram

Bits	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
R								IBEn								
W																
Reset	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Bits	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
R								IBEn								
W																
Reset	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

20.4.8.4   Fields

Field	Function
0-31 IBEn	Input Enable. Indicates whether a GPIO port's input is enabled. 0000000000000000000000000000000b - The corresponding port input is disabled. 0000000000000000000000000000001b - The corresponding port input is enabled.