



Figure 8 — Example of DQ Wiring with Mapping for CRC

Table 21 — Example of DQ Mapping for CRC

DQ bit of DIMM Connector																			
0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19
First x8 SDRAM								Second x8 SDRAM											
5	7	4	6	DQ mapping for first nibble matches index 0x2B. 0x2B would be stored in the SPD table for the first nibble.															
3 1 0 2				DQ mapping of second nibble matches index 0x15. 0x15 would be stored in the SPD table for the second nibble.															
1 3 2 0				0x0C for the 3rd nibble.															
0x35 for the fourth nibble.				7 5 4 6															