

MOTOROLA Motorola MCSL Roznov
1. maja 1009
756 61 Roznov p.R., Czech Republic, Europe

MCSL

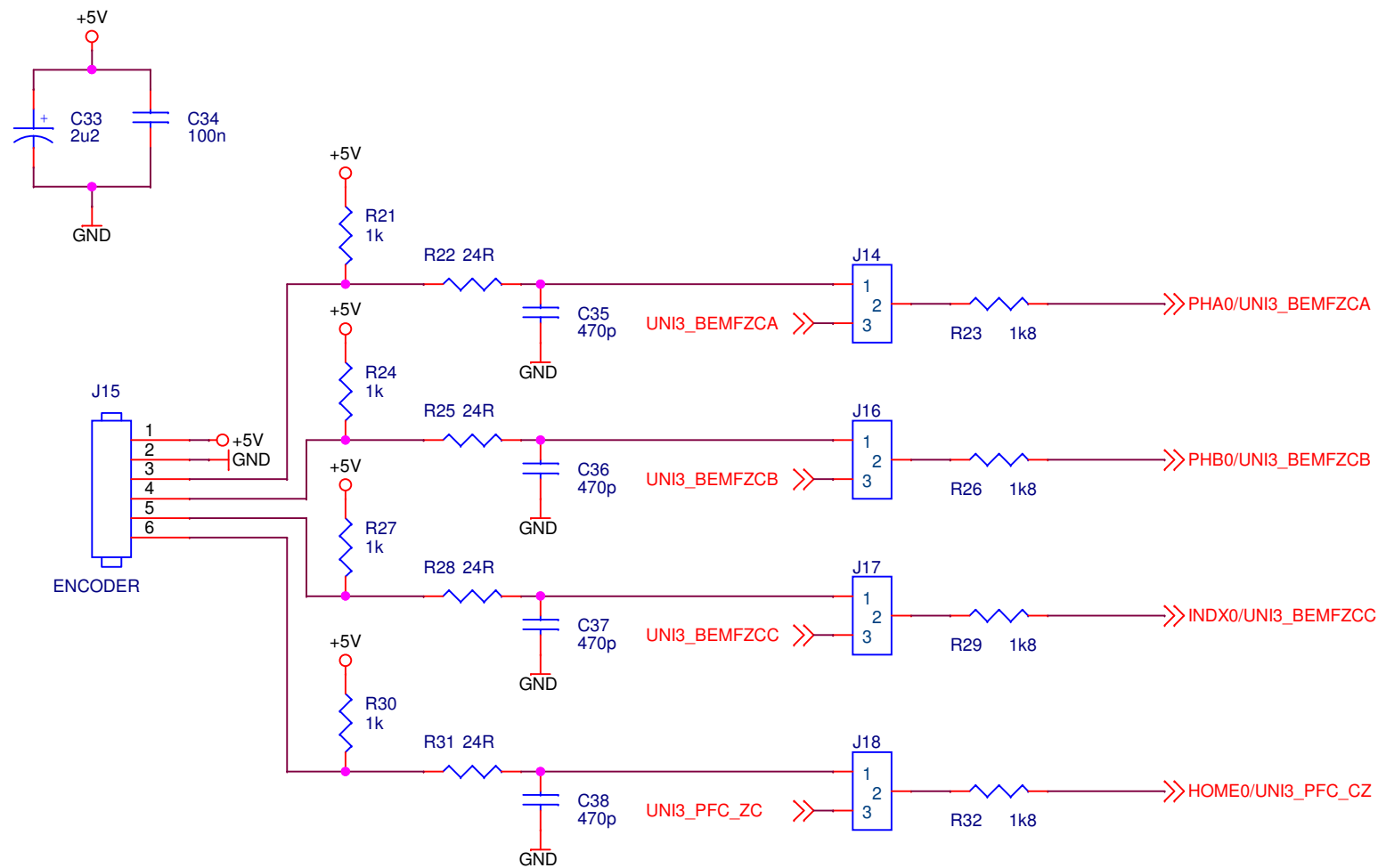
Title: **S12E128 Controller Board**


Author: Radim Visinka

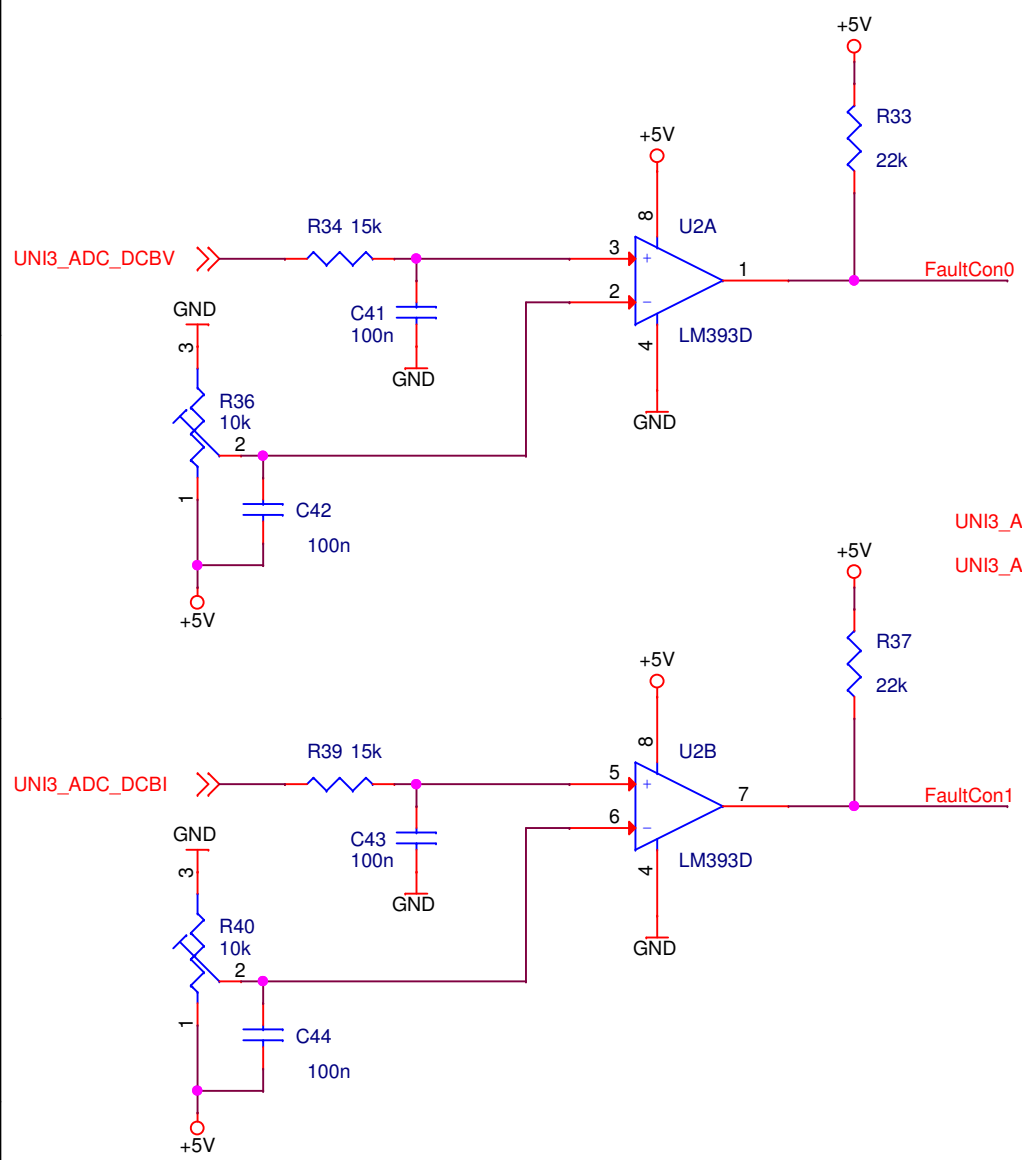
Design Name:

Schematic Name: 157	Rev 02
Modify Date: Tuesday, February 17, 2004	Size B
Sheet 1 of 8	
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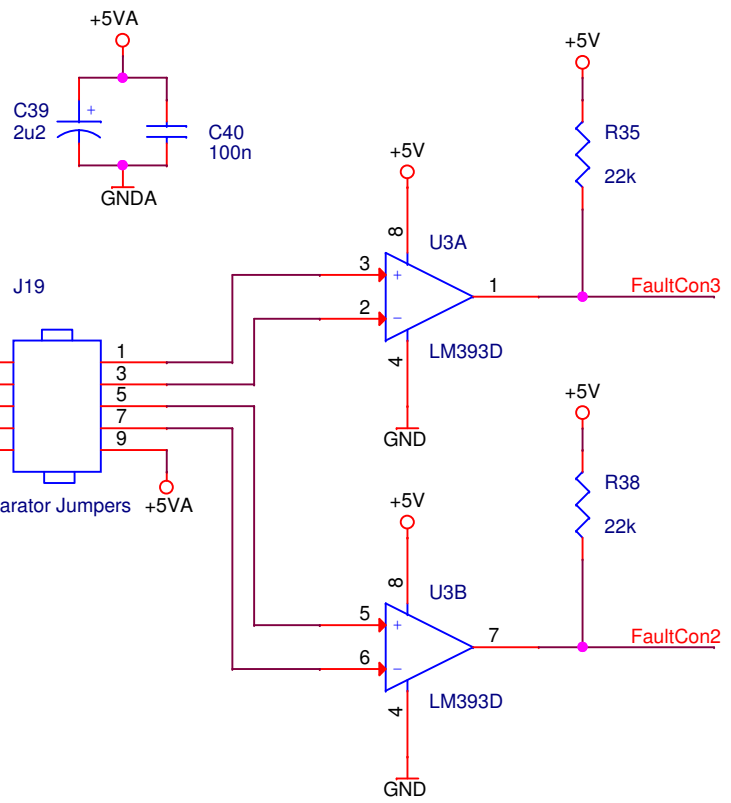
00187_02



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	Title S12E128 Controller Board
Author: Radim Visinka	
Design Name:	
Schematic Name: 157	Rev 02 Size A
Modify Date: Tuesday, February 17, 2004	Sheet 2 of 8
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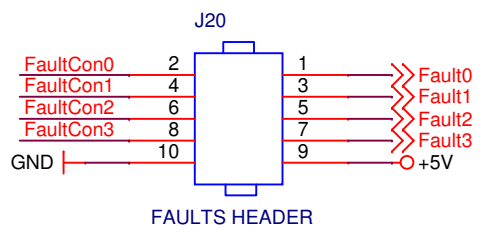


Place close to
Comparator Jumpers

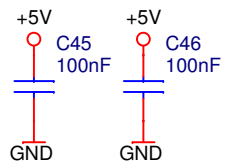



UNI3_ADC_PHA1
DAC1
UNI3_ADC_PHBI
DAC0
GNDA

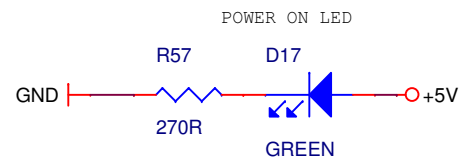
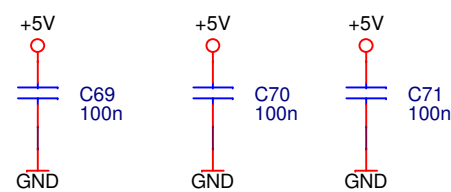
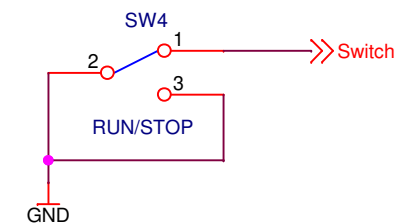
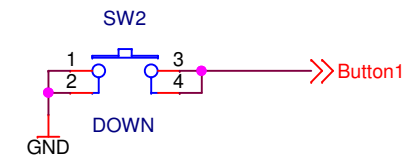
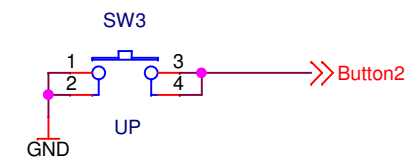
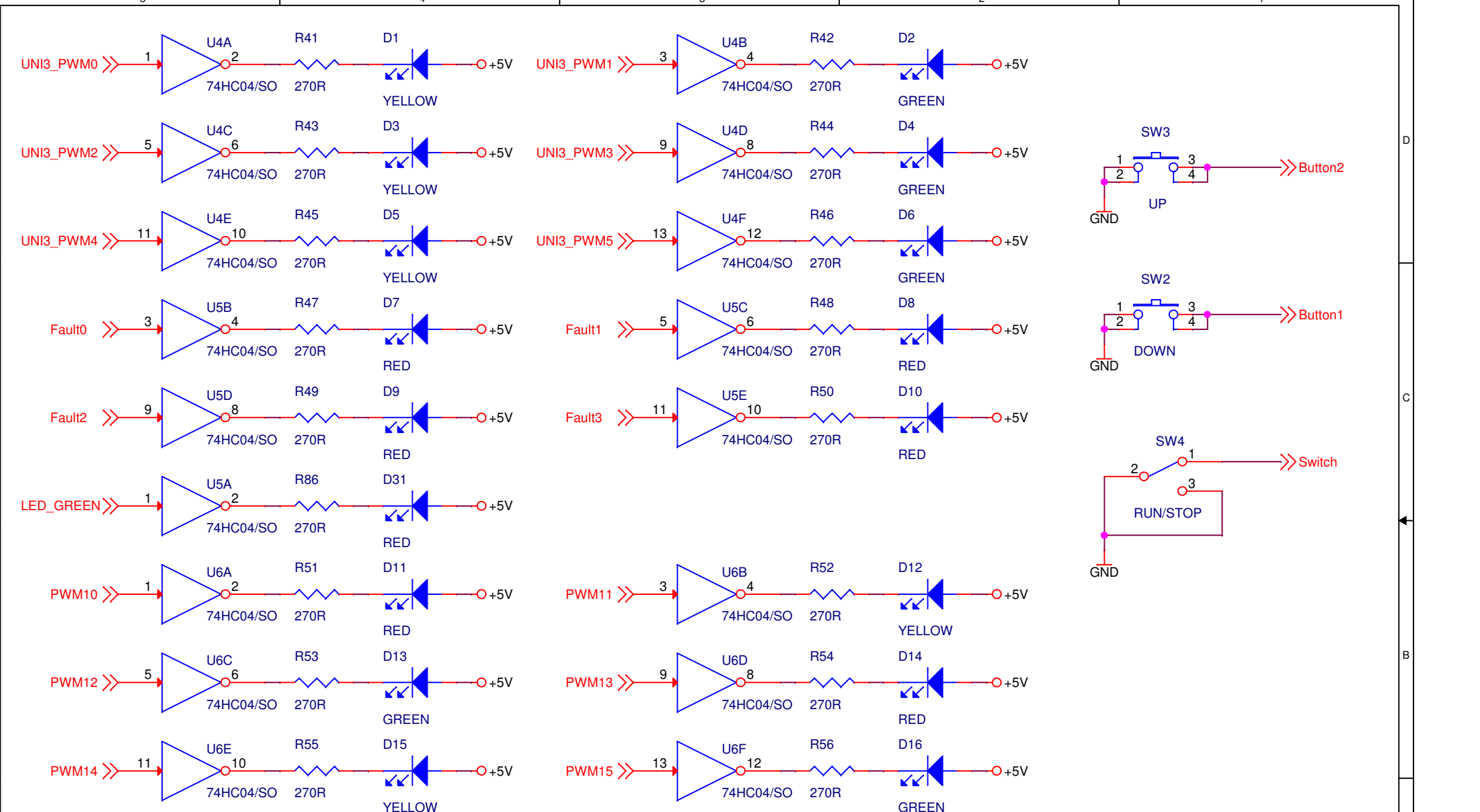
Comparator Jumpers +5VA



Place close to
Comparators

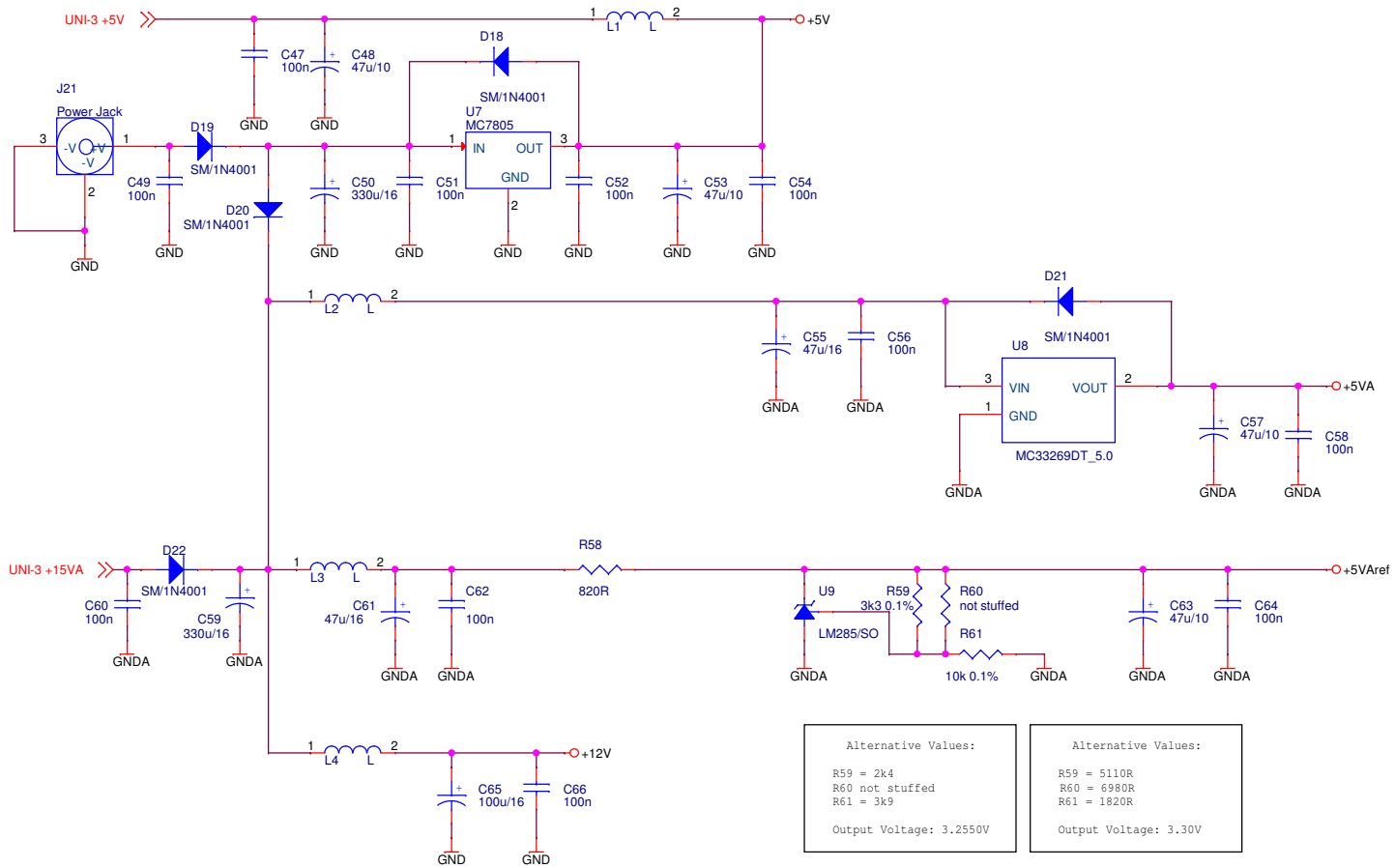


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	Title S12E128 Controller Board	
Author: Radim Visinka Design Name:		
Schematic Name: 157		Rev 02 Size A
Modify Date: Tuesday, February 17, 2004		Sheet 3 of 8
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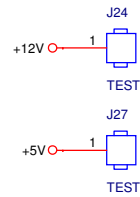
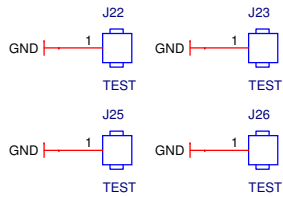
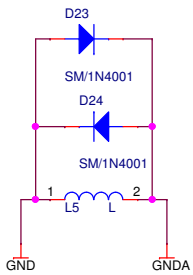
Place close to individual LED buffers

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Schematic Name: 157		Rev 02 Size A	
Modify Date: Tuesday, February 17, 2004		Sheet 4 of 8	
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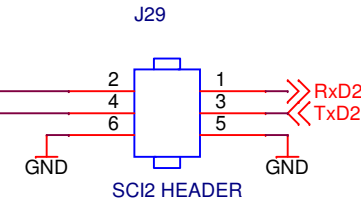
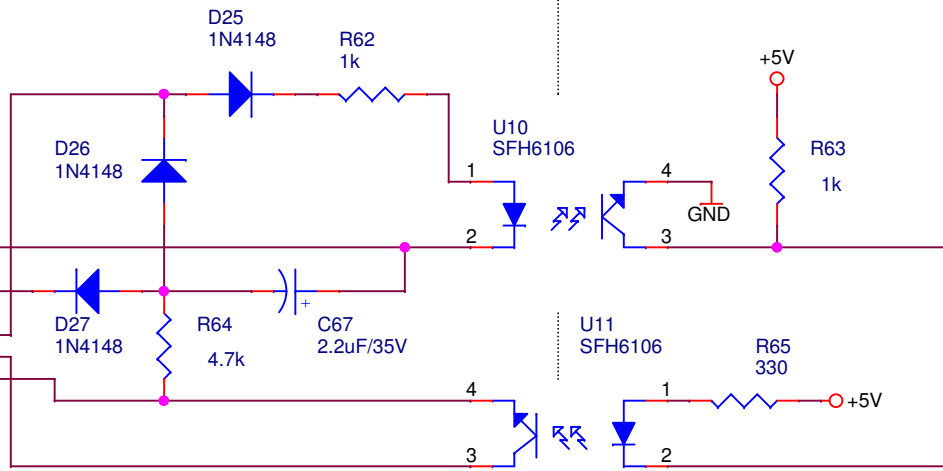
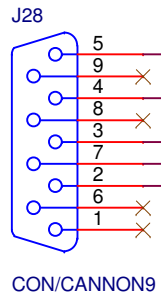
Alternative Values:
 R59 = 2k4
 R60 not stuffed
 R61 = 3k9
 Output Voltage: 3.2550V

Alternative Values:
 R59 = 5110R
 R60 = 6980R
 R61 = 1820R
 Output Voltage: 3.30V



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Design Name:			
Schematic Name: 157		Rev	02
		Size	B
Modify Date: Tuesday, February 17, 2004		Sheet	5 of 8
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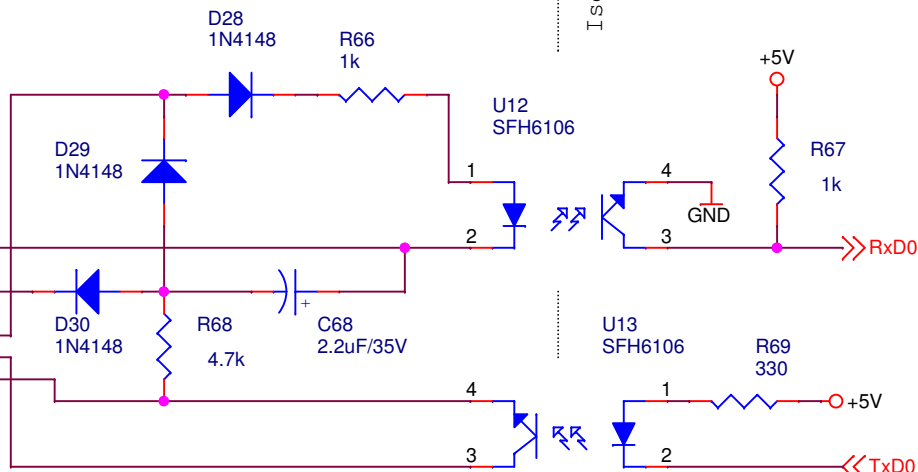
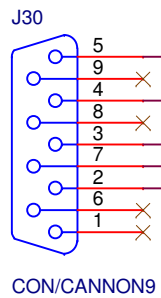
Secondary
RS232



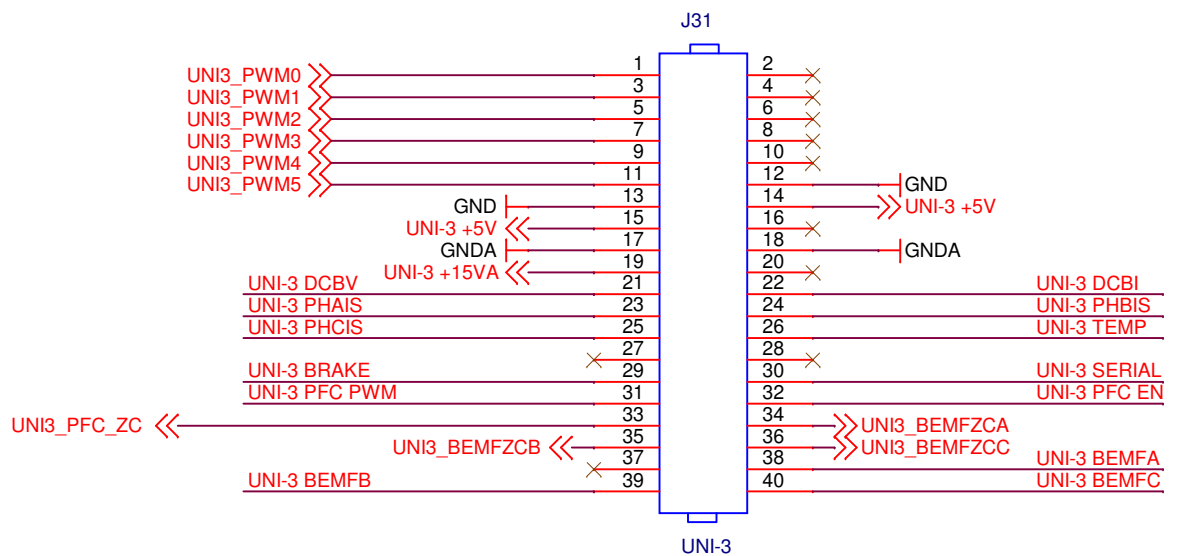
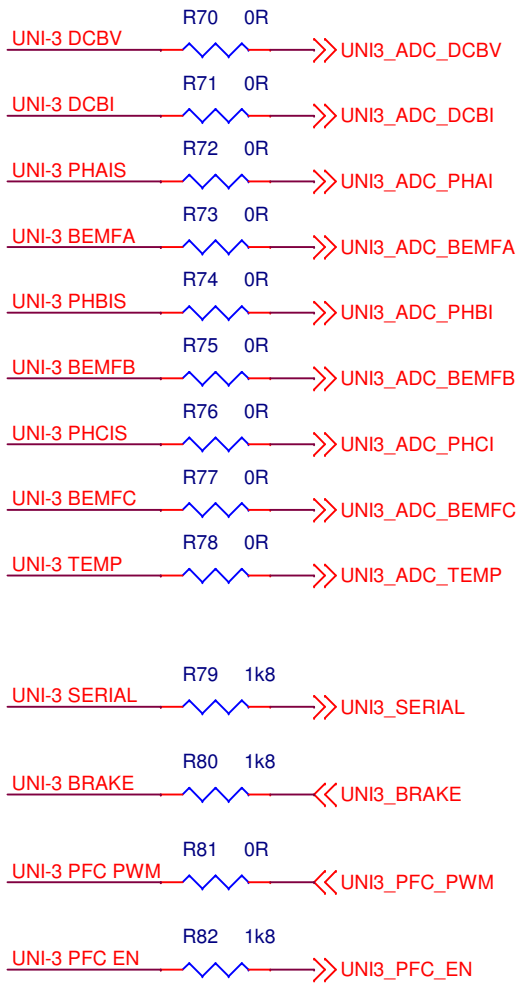
Isolation Barrier

Isolation Barrier

Primary
RS232



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4

3

2

1

D

D

C

C

B


B

A

A

REVISIONS				
Zone	Rev	Description	Date	Approved
	01	Initial Revision	14.11.2002	R.V.
	02	Corrected version	16.2.2004	R.V.

00157_02

 MOTOROLA <i>MCSL</i>	Motorola MCSL Roznov 1. maje 1009 756 61 Roznov p.R., Czech Republic, Europe
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