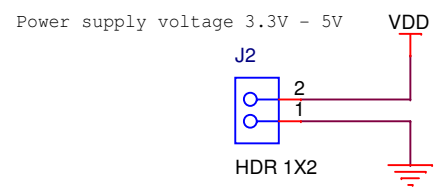


Place C4, C5, C6 to pins VDDX1, VDDX2, VDDR as close as possible



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ICAP Classification:		FCP:	FIUO: X	PUBI:
Designer: <Designer>	Drawing Title: <b>&lt;Title&gt;</b>			A
Drawn by: <DrawnBy>	Page Title: <b>&lt;PageTitle&gt;</b>			
Approved: <Approver>	Size A	Document Number SCH-XXXXX PDF: SPF-XXXXX	Rev X	
Date:		Monday, April 23, 2012	Sheet	1 of 1